



SP6007

Fast Turn-off Intelligent Rectifier

DESCRIPTION

The SP6007 is a low-drop diode emulator controller IC which when combined with an external MOS FET replaces Schottky diodes in high-efficiency flyback converters. The chip regulates the forward drop of an external MOS FET to about 40mV and switches it off as soon as the voltage becomes negative.

SP6007 is available in space saving SOT-23-6 package.

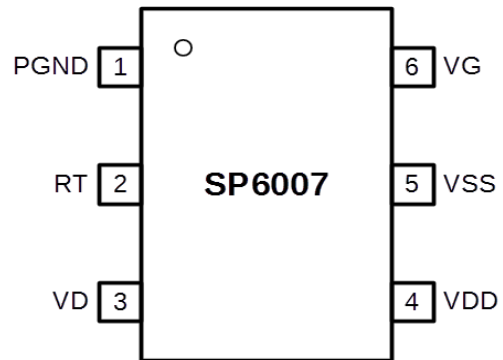
FEATURES

- Works with 5V Logic Level FETS
Less Than 100mW Standby Power
- Fast Turn-off Delay of 25ns
- 3.6V~5.5V VDD operating range
- Supports DCM and Quasi-Resonant Operation
- Supports High-side and Low-side Rectification
- Available in space saving SOT-23-6 Package

APPLICATIONS

- Industrial Power Systems
- Distributed Power Systems
- Battery Powered Systems
- Flyback Converters

PIN CONFIGURATION (SOT-23-6L)



PART MARKING



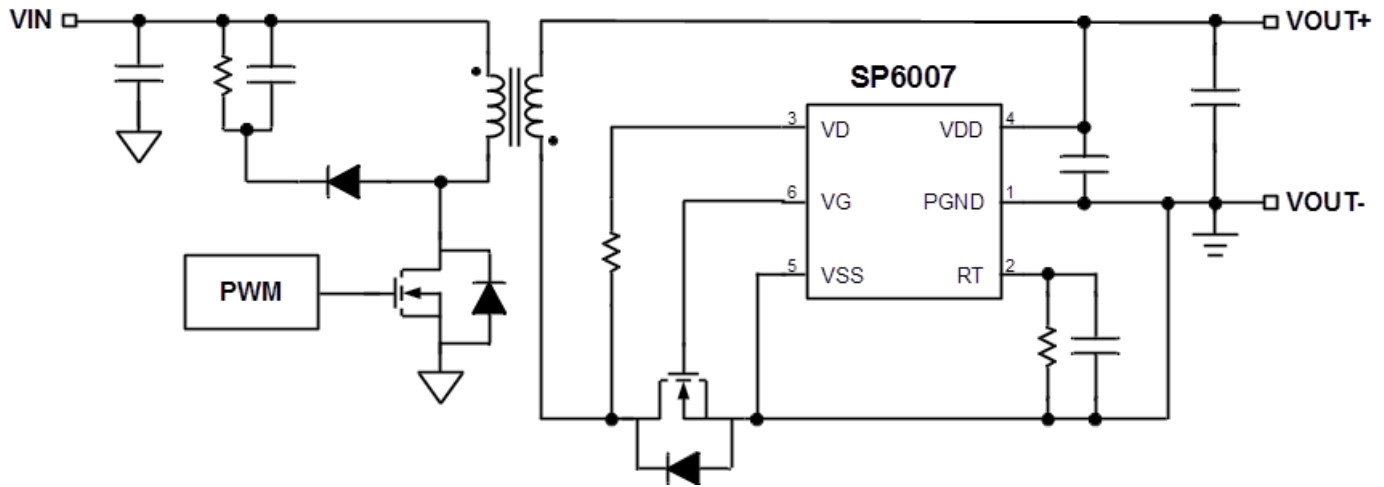
Y : Year Code
W : Week Code



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TYPICAL APPLICATION CIRCUIT



PIN DESCRIPTION

Pin No.	Pin Name	Description
1	PGND	Power Ground, return for gate driver
2	RT	Minimum On-time setting pin. A resistor connected between this pin and VSS defines minimum On-time
3	VD	External FET drain voltage sensing
4	Vdd	DC supply voltage.
5	VSS	Ground, also used as reference for VD
6	VG	Gate driver output

ORDERING INFORMATION

Part Number	Package	Part Marking
SP6007S26RGB	SOT-23-6L	SP6007

※ SP6007S26RGB : Tape Reel ; Pb – Free ; Halogen – Free



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ELECTRICAL CHARACTERISTICS

($T_A=25^\circ\text{C}$, $V_{DD}=5\text{V}$, $R_{RT} = 100\text{k}\Omega$, unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply Section						
V _{DD}	Supply voltage		3.6		5.5	V
V _{DD on}	V _{DD} UVLO rising				3.6	V
V _{DD} hysteresis	V _{DD} UVLO hysteresis		0.2			V
I _{CC}	Operating current	C _{LOAD} =5nF, F _{sw} =100kHz			10	mA
I _q	Quiescent current	V _{SS} -V _D =0.5V			3	mA
	Shutdown current	V _{DD} =3V			100	uA
	Thermal shutdown			150		°C
	Thermal shutdown hysteresis			30		°C
Control Circuitry Section						
V _{fwd}	V _{SS} -V _D forward voltage			40		mV
	V _{SS} -V _D turn-off threshold			20		mV
T _{Don}	Turn-on delay	C _{LOAD} =5nF		100		nS
		C _{LOAD} =10nF		150		nS
	Input bias current on VD pin	V _D =60V			1	uA
T _{MIN}	Minimum on-time	C _{LOAD} =5nF		1.6		uS
V _{Boff}	Turn-off blanking V _{DS}			2		V
V _{ON-DS}	Turn-on V _{DS} threshold			-250		mV
Gate Driver Section						
V _{G-L}	Gate output low voltage	I _{LOAD} =1mA			0.1	V
V _{G-H}	Gate output high voltage	V _{DD} =5V	4.5			V
	Turn-off propagation delay	V _D =V _{SS}		25		nS
T _{Doff}	Turn-off total delay	V _D =V _{SS} , C _{LOAD} =5nF, R _{GATE} =0Ω, V _{GS} =2V		35		nS
		V _D =V _{SS} , C _{LOAD} =10nF, R _{GATE} =0Ω, V _{GS} =2V		45		nS
	Maximum source current (*)			0.5		A
	Maximum sink current (*)		2			A
	Pull down impedance			1		Ω

Notes:

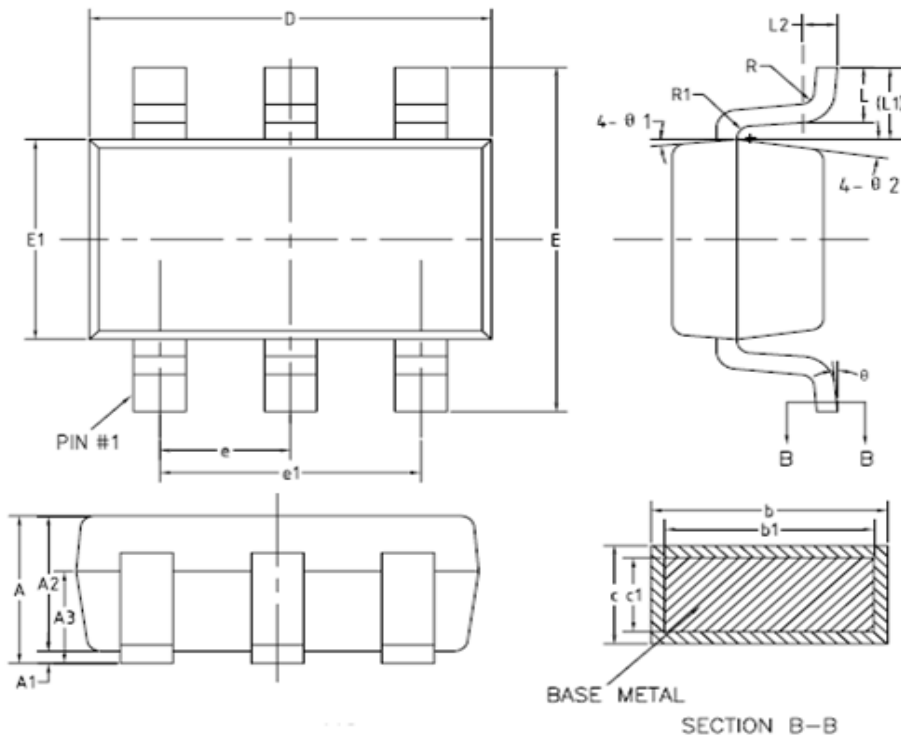
(*) Guaranteed by design and characterization



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SOT-23-6L PACKAGE OUTLINE



SYMBOL	MIN	NOM	MAX
A	1.05	--	1.30
A1	0.00	--	0.15
A2	0.90	1.10	1.30
A3	0.60	0.65	0.70
b	0.30	--	0.50
b1	0.32	--	0.45
c	0.10	--	0.25
c1	0.10	--	0.20
D	2.82	2.92	3.05
E	2.60	2.80	3.00
E1	1.50	1.60	1.75
e	0.95 REF		
e1	1.80	1.90	2.00
L	0.35	0.45	0.60
L1	0.59 REF		
L2	0.25BSC		
θ	0°	--	8°
$\theta 1$	3°	5°	7°
$\theta 2$	6°	8°	10°



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