



SP6019D

Synchronous Rectifier Driver

DESCRIPTION

The fundamental of SP6019D synchronous rectifier (SR) driver IC is based on our U.S. patented methods that utilize the principle of “prediction” logic circuit. The IC deliberates previous cycle timing to control the SR in present cycle by “predictive” algorithm that makes adjustments to the turn-off time, in order to achieve maximum efficiency and avoid cross-conduction at the same time. SP6019D is specially suitable for Forward and DC/DC Module.

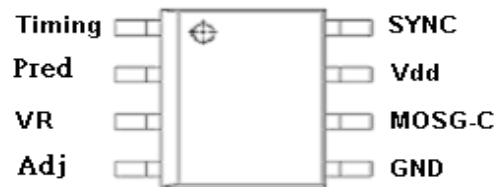
APPLICATION

- Servers & workstations
- Storage area network power supplies
- Telecommunication converters
- Embedded systems
- Industrial & commercial systems using high current processors
- DC/DC Power Module

FEATURES

- Offers efficiency improvement over Schottky Diode (depends on drive configuration of the SR).
- Drives all Power MOSFET.
- Prediction gate timing control.
- Minimum MOSFET body diode conduction.
- Operating at high switching frequency.
- Synchronize to transformer secondary voltage waveform.
- Linear setting of timing function.
- SOP-8 Package

PIN CONFIGURATION (SOP-8)



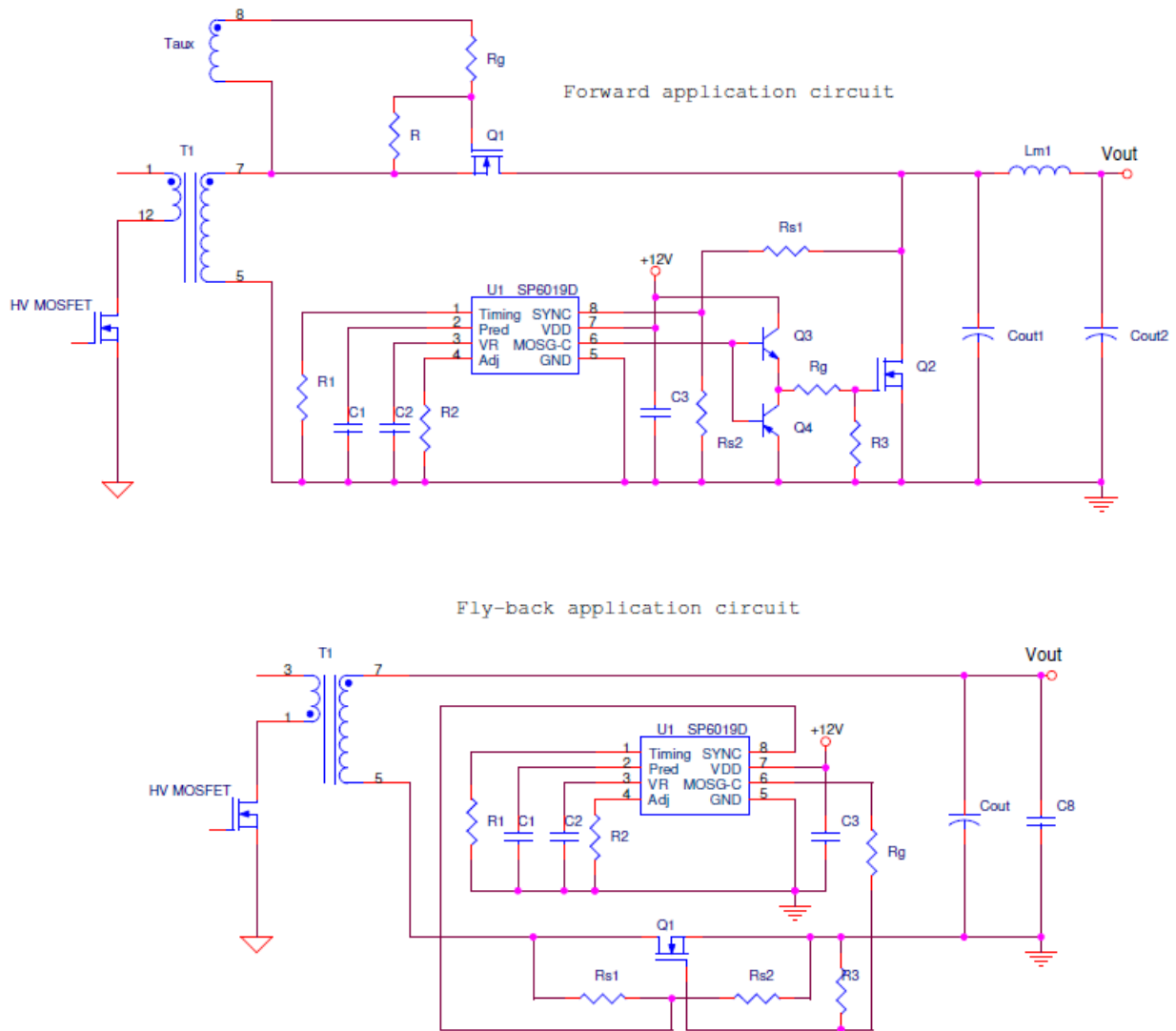
PART MARKING





SP6019D Synchronous Rectifier Driver

TYPICAL APPLICATION CIRCUIT



PIN DESCRIPTION

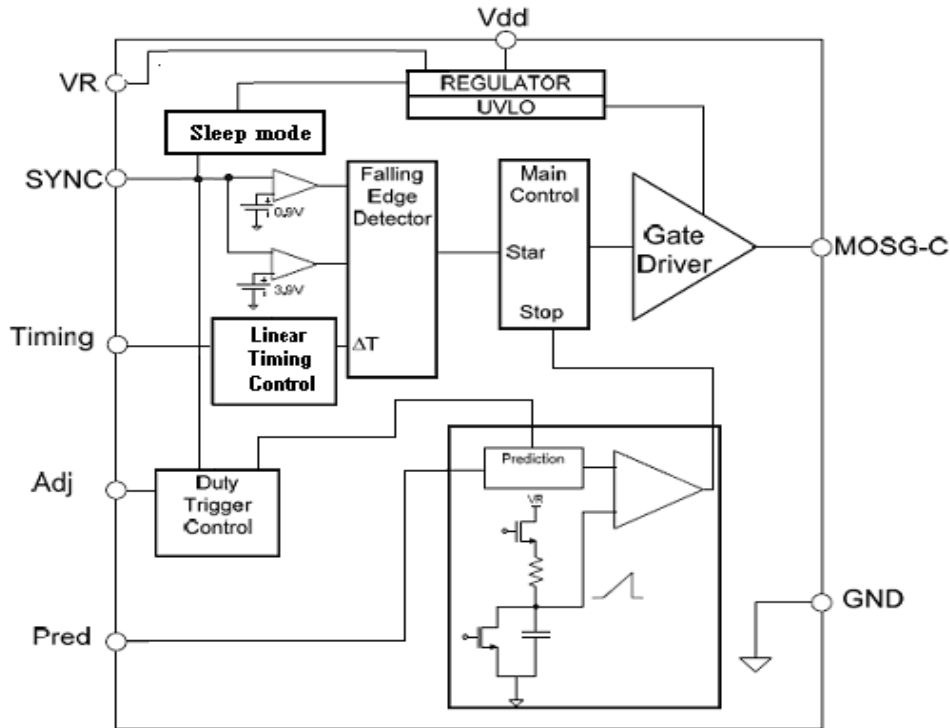
Pin	Symbol	Description
1	Timing	Discontinuous current filter timing adjustment resistor connection.
2	Pred	Capacitor to store previous cycle timing for SR MOSFET.
3	VR	Voltage Regulator.
4	Adj	Trigger point adjustment for Dynamic state.
5	GND	Ground connection.
6	MOSG-C	Catch MOSFET gate drive.
7	Vdd	DC supply voltage.
8	SYNC	Synchronized signal from the V_{DS} of SR MOSFET.



SP6019D

Synchronous Rectifier Driver

BLOCK DIAGRAM



ORDERING INFORMATION

Part Number	Package	Part Marking
SP6019DS8RGB	SOP-8	SP6019D
SP6019DS8TGB	SOP-8	SP6019D

※ SP6019DS8RGB : Tape Reel ; Pb – Free ; Halogen - Free

※ SP6019DS8TGB : Tube ; Pb – Free ; Halogen – Free

ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified.)

The following ratings designate persistent limits beyond which damage to the device may occur.

Symbol	Parameter	Value	Unit
V _{dd}	DC Supply Voltage	16	V
V _{SYNC/MOSG-C}	Sync/Output Voltage	-0.6~16	V
V _{R/Timing/pred/Adj}	Regulator/ Timing/Pred/Adj Voltage	-0.6~6	V
I _{OUT}	Peak Source Current (Pulsed)	2.0	A
	Peak Sink Current (Pulsed)	2.0	A
P _D	Power Dissipation @ TA=85°C SOP-8 (*)	0.25	W
T _J	Operating Junction Temperature Range	-40 to 125	°C
T _{STG}	Storage Temperature Range	-40 to 150	°C
T _{LEAD}	Lead Soldering Temperature for 5 sec.	260	°C

THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance Junction – Case SOP-8 (*)	45	°C/W

(*) The power dissipation and thermal resistance are evaluated under copper board mounted with free air conditions.



SP6019D

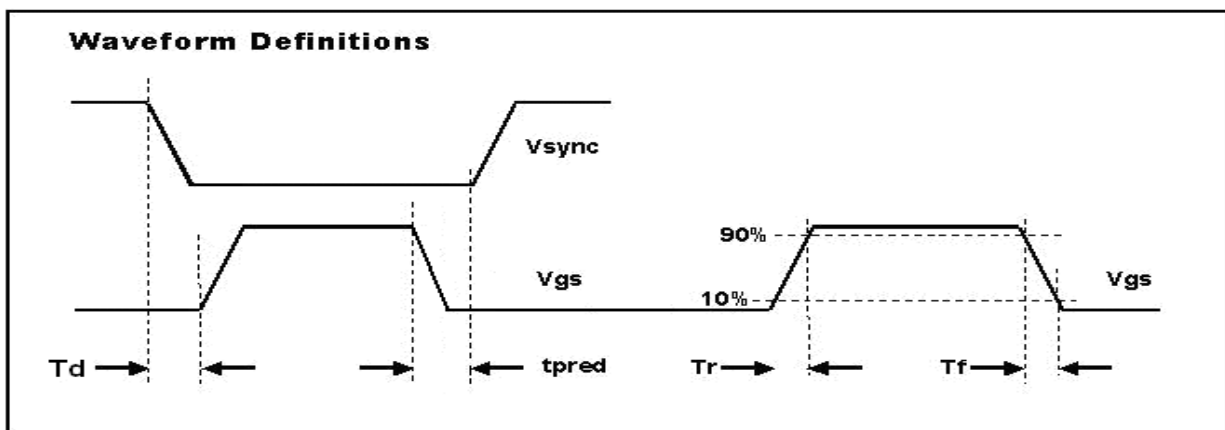
Synchronous Rectifier Driver

ELECTRICAL CHARACTERISTICS

($T_A=25^{\circ}\text{C}$, $V_{dd}=12\text{V}$, Freq. =50 KHz, Duty Cycle=50%, unless otherwise specified.)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
SUPPLY INPUT						
IDD	Supply current	No load		0.15		mA
		$V_{\text{SYNC}}=0\text{V}$, $V_{\text{dd on}}$ $I_{\text{dd peak}} < 2\text{A}$		3	4	mA
Vdd	Supply voltage				16	V
Vdd on	Enable voltage		7.8	8.2	8.6	V
Vdd hysteresis	Enable voltage			0.25	0.5	V
Vovp	Over voltage protection		15.5	16.5	17.5	V
Vovp hystersis				0.3		V
SYNC REFERENCE (SYNC)						
Vshth	SYNC high threshold		3.5	3.9		V
Vslth	SYNC low threshold			0.9	1.2	V
Vsync	SYNC wake-up voltage	$I_{\text{sync}}=3\text{mA}$	9		16	V
Isync	SYNC input current				3	mA
Voltage Regulator REFERENCE (VR)						
VR	voltage		5.2		5.4	V
Ivr	VR Output Current				50	mA
ON TIME DUTY SETUP (PIN 6)						
Ton-time				26	32	us
MOSFET GATE DRIVER (MOSG-C)						
Voh	Output high voltage	$I_o = -200\text{mA}$	10.5	11.0		V
Vol	Output low voltage	$I_o = 200\text{mA}$		0.5	0.8	V
Td	Propagation delay	No load	25	50	155	ns
Tpred		No load		120		ns
Tr	Rise time	Load = 1nF (*)		10	25	ns
Tf	Fall time	Load = 1nF (*)		10	25	ns
Dynamic Protect						
Dt	Dynamic variable	Pin 4 open		600		ns
Ton-min	MOSG-C on time	PWM adjusts time > Dt		0.5		us

(*) Tr & Tf are measured among 10% and 90% of starting and final voltage.

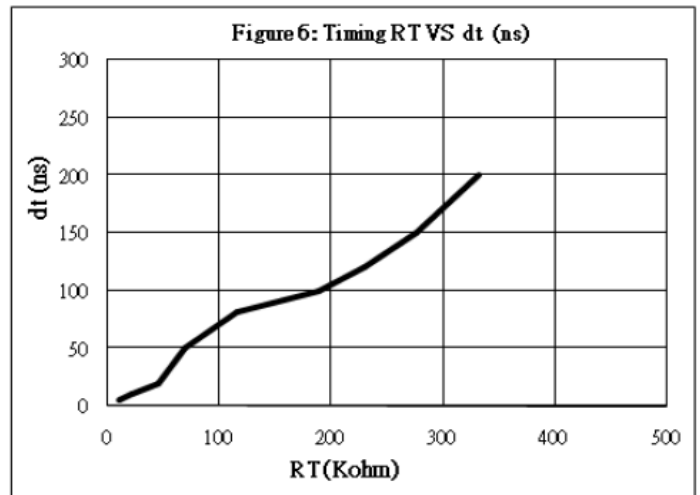
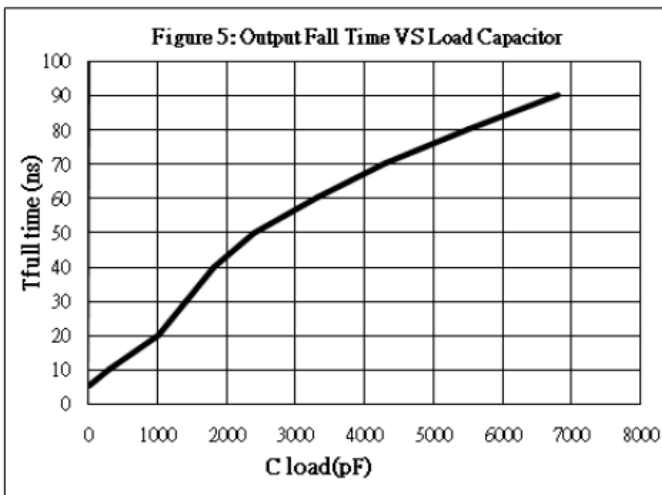
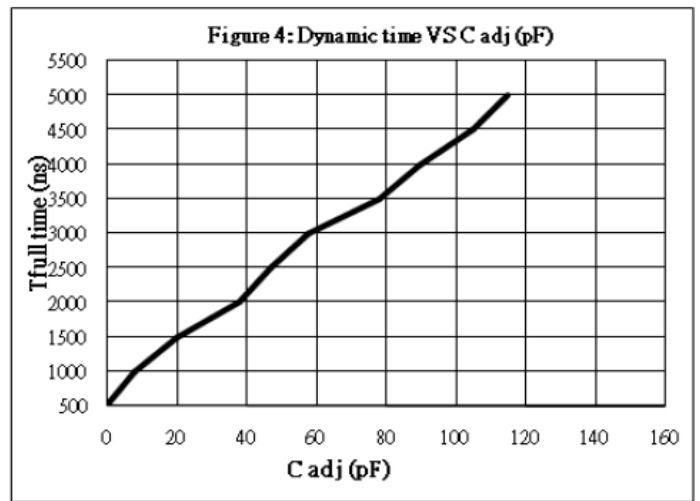
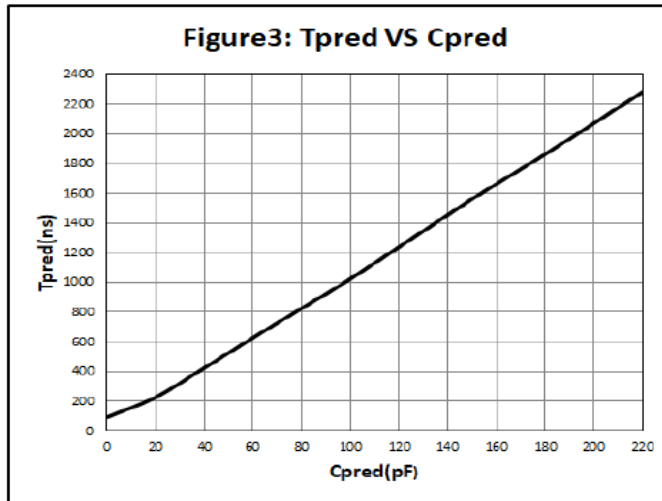
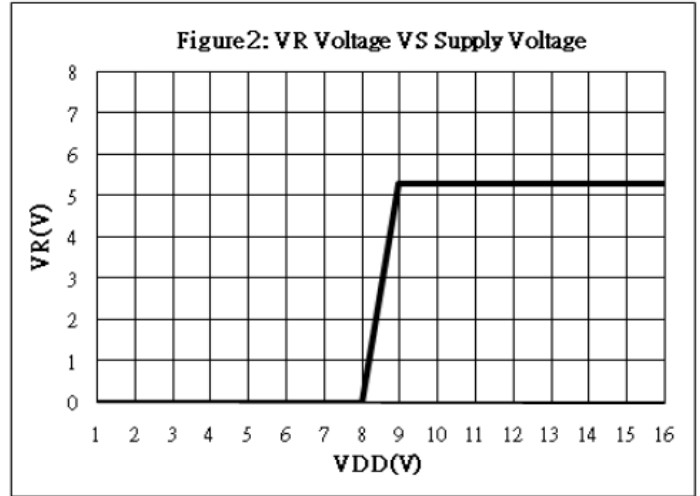
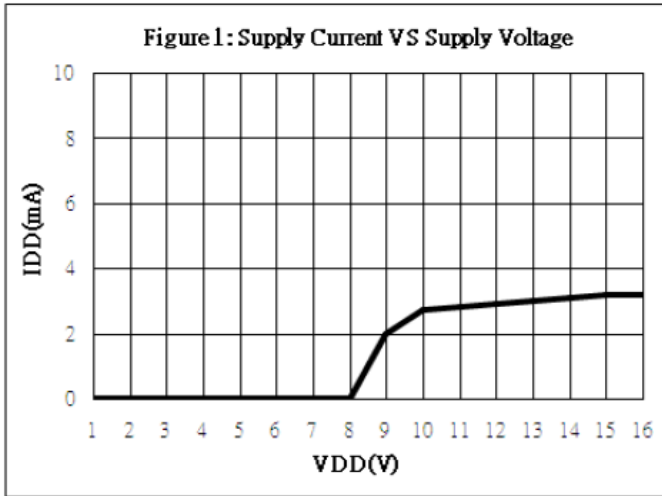




SP6019D

Synchronous Rectifier Driver

PERFORMANCE CHARACTERISTICS ($T_A=25^{\circ}\text{C}$, unless otherwise specified.)



*Fig. 1 : No Load ; No SYNC

*Fig. 3 : Frequency = 100 kHz

*Fig. 4~5 : Frequency = 65 kHz.

*Fig. 6 : The falling time of negative edge from 3.9V to 0.9V



SP6019D

Synchronous Rectifier Driver

Information provided is alleged to be exact and consistent. SYNC Power Corporation presumes no responsibility for the penalties of use of such information or for any violation of patents or other rights of third parties, which may result from its use. No license is granted by allegation or otherwise under any patent or patent rights of SYNC Power Corporation. Conditions mentioned in this publication are subject to change without notice. This publication surpasses and replaces all information previously supplied. SYNC Power Corporation products are not authorized for use as critical components in life support devices or systems without express written approval of SYNC Power Corporation.

© The SYNC Power logo is a registered trademark of SYNC Power Corporation

© 2020 SYNC Power Corporation – Printed in Taiwan – All Rights Reserved

SYNC Power Corporation

7F-2, No.3-1, Park Street

NanKang District (NKSP), Taipei, Taiwan, 115, R.O.C

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

<http://www.syncpower.com>