SP6876 Multi-Mode PWM Controller

DESCRIPTION

SP6876 is a high performance flyback multi-mode PWM controller, optimized to achieve high efficiency and low standby power with effective system cost.

At full load IC operates in CCM mode with fixed frequency at low line input range and in QR mode at high line input range. At normal load conditions it operates in OR mode with internally limited frequency to 75kHz typ. to minimize switching loss. Controller gradually reduces frequency with loading at light load conditions to keep high efficiency and switches to extended burst mode at no-load conditions to minimize stand-by power loss. As a result, high conversion efficiency can be achieved with universal input range and within whole loading range.

The rich set of protection features such as VCC Under Voltage Lockout (UVLO), VCC Over Voltage Protection (VCC OVP) and clamp, load Over Voltage Protection (OVP), Over Load Protection (OLP) and onchip Over Temperature Protection (OTP) helps to build low component counts and high performance power supply.

The tone energy at below 23KHz is minimized in the design and audio noise is eliminated during operation. SP6876 is offered in space saving SOT23-6 pack

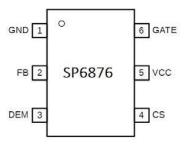
FEATURES

- Internal soft start reducing MOS FET VDS stress
- Multi-mode operation for better efficiency
- Frequency shuffling for better EMI
- Extended burst mode for lower standby power loss
- The rich set of protection features:
 - o VCC Under Voltage Lockout (UVLO) with hysteresis
 - o VCC Over Voltage Protection (VCC OVP) and
 - o Cycle-by-cycle current limiting with line voltage compensation
 - o Over Load Protection (OLP) with autorecovery
 - o Adjustable load Over Voltage Protection (OVP)
 - o Fixed on-chip and adjustable external over temperature protection (OTP) with autorecovery

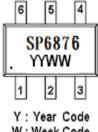
APPLICATIONS

- AC/DC switching power adaptor
- Set-top box power supply
- Open-frame switching power supply
- **NB** Adaptor
- TV/Monitor Standby Power
- **PC** Peripherals

PIN CONFIGURATION (SOT-23-6L)



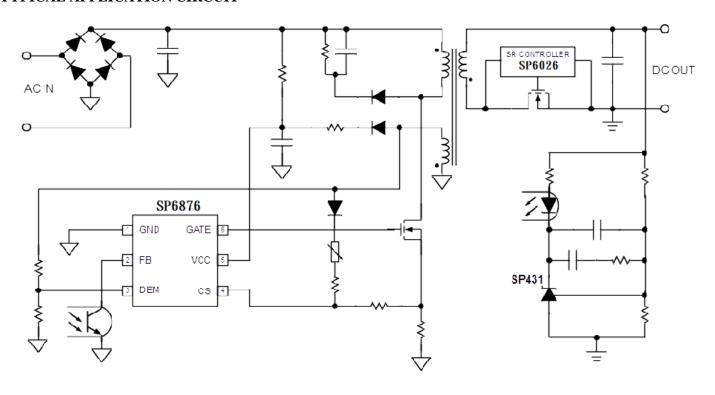
PART MARKING



W: Week Code



TYPICAL APPLICATION CIRCUIT



PIN DESCRIPTION

Pin No.	Pin Name	Description		
1	GND	Ground pin		
2	FB	Feedback pin. The PWM duty cycle is determined by voltage level on this pin and the current sense signal at pin 4		
3	DEM	Multiple function pin. Transformer demagnetization and load OVP detection pin		
4	CS	Current sense pin. Also can be used for external over temperature protection with connected to an auxiliary winding of the PWM transformer a NTC resistor and a diode		
5	VCC	IC power supply pin		
6	GATE	Gate driver output pin		

ORDERING INFORMATION

Part Number	Package	Part Marking		
SP6876S26RGB	SOT-23-6L	SP6876		

※ SP6876S26RGB: Tape Reel; Pb − Free; Halogen − Free

BLOCK DIAGRAM Internal vcc ₫-Regulator supply Soft S Q 6 GATE Driver OTP Timer, Q Logic & Fault Management Soft OVP Start External UVLO **→** POR -⊈ CS LEB Detector Mode Selection 2 FB Load OVP DEM 3 1 GND

PROTECTION MODE

110120101(11022						
	Part number	VCC_OVP	OSCP		OLP	OTP
	SP6876	Latch off	Auto-Restart	Latch off	Auto-Restart	Auto-Restart

 $\label{eq:absolute_maximum_ratings} \textbf{ABSOLUTE MAXIMUM RATINGS} \ (T_A = 25\,^{\circ}\text{C}, \text{ unless otherwise specified})$ The following ratings designate persistent limits beyond which damage to the device may occur.

Symbol	Parameter		Unit			
V_{CC}	Zener clamp voltage (@ 10mA)	31	V			
V_{FB}, V_{CS}, V_{DEM}	FB, CS, DEM pin voltage	-0.3 ~ 7.0	V			
T_{OP}	Operating ambient temperature	-40 ~ 85	$^{\circ}\!\mathrm{C}$			
$T_{ m J}$	Operating junction temperature	-40 ~ 150	$^{\circ}\!\mathrm{C}$			
T_{STG}	Storage temperature	-40 ~ 150	$^{\circ}$ C			
T_{LEAD}	Lead soldering temperature for 5 sec	260	$^{\circ}$ C			
THERMAL RESISTANCE						
Rөjc	Thermal Resistance Junction – Case (*)	110	°C/W			



ELECTRICAL CHARACTERISTICS

 $(T_A=25^{\circ}C, V_{CC}=16V, unless otherwise specified)$

Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
Supply Voltage Section (VCC pin)								
Istup	Start-up current	V _{CC} =UVLO(OFF)-		5	15	uA		
	•	1.5V						
Icc(op)	Operating current	V _{FB} =3V		2	4	mA		
	•	$V_{FB}=3V$, $C_L=1nF$		3	5	mA		
Icc(burst)	Burst mode operating current	$V_{FB} = 0.5V, V_{CS} = 0V$		0.6	0.8	mA		
UVLO(ON)	VCC under voltage lockout enter			8		V		
UVLO(OFF)	VCC under voltage lockout exit (recovery)			18.5		V		
OVP	VCC over voltage protection			28		V		
Vcc_clamp	Clamping voltage	I _{CC} =10mA		31		V		
Feedback Se	ection (FB pin)	<u>.</u>			•			
I _{FB(SC)}	Short circuit current			250		uA		
V _{FB} Open	Open loop voltage			5.3		V		
$V_{TH_BM_off}$	Burst mode exit threshold			0.8		V		
V _{TH_BM_on}	Burst mode enter threshold			0.7		V		
$V_{FB(OLP)}$	Over load protection threshold			4.4		V		
T _{DEL(OLP)}	Over load protection delay			80		ms		
	ation Detection Section (DEM pin)							
$V_{\mathrm{DEM(TH)}}$	Demagnetization threshold			75		mV		
$V_{\mathrm{DEM(H)}}$	Upper clamp voltage			6		V		
$V_{\mathrm{DEM(L)}}$	Lower clamp voltage			-0.7		V		
$T_{DEL(DEM)}$	Demagnetization propagation delay			250		ns		
$V_{\mathrm{DEM(OVP)}}$	Load over voltage protection threshold			3.75		V		
T _{DEL(OVP)}	Number of subsequent cycles to trigger OVP			4		Cycle		
Current Sen	se Section (CS pin)	<u>.</u>			•			
$V_{CS(L)}$	CS threshold at zero duty cycle	$V_{FB}=3V$	0.415	0.45	0.485	V		
$V_{CS(H)}$	CS threshold at max duty cycle	$V_{FB}=3V$		0.8		V		
V _{CS(BM)}	CS threshold at burst mode	V _{FB} =1V		0.3		V		
T_{LEB}	Leading edge blanking time			300		Ns		
T _{DEL(CS)}	Over current detection and control delay			80		ns		
V _{TH(OTP)}	External OTP threshold			0.25		V		
T _{DEL(OTP)}	External OTP debounce time			50		ms		
Soft Start Section								
T_{SS}	Soft start time			4		ms		
Internal OT	P							
T_{SD}	Thermal shutdown threshold			150		°C		
T_{SD_HYS}	Thermal shutdown hysteresis			30		°C		



ELECTRICAL CHARACTERISTICS (continued)

(T_A=25°C, V_{CC}=16V, unless otherwise specified)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit			
Oscillator									
F_{BURST}	Burst mode switching frequency			22		kHz			
$F_{QR(L)}$	Frequency low clamp in QR mode		47	52	57	kHz			
$F_{QR(H)}$	Frequency high clamp in QR mode			75		kHz			
G_PFM	Frequency reduction ratio			60		kHz/V			
$\Delta F_{\text{(shuffle)}} / F$	Frequency shuffling range		-4		+4	%			
T_{ON}	Maximum ON time		10	12.5	15	us			
T_{OFF}	Maximum OFF time		40	55	75	us			
Gate Driver	Gate Driver Section (GATE pin)								
$V_{O(L)}$	Output low voltage	I _O =10mA			1	V			
$V_{O(H)}$	Output high voltage		11.5			V			
V _{O(CLAMP)}	Output clamp voltage	V _{CC} =20V		16.5		V			
tr	Voltage rise time	$C_L=1nF$		100		ns			
tf	Voltage fall time	$C_L=1nF$		50		ns			

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