

DESCRIPTION

The SP6878 is a low cost, quasi-resonant flyback controller where the maximum frequency is below 100KHz. The internal valley detector ensures the converter operates at quasi-resonant operation over wide range of line voltage. The build-in advanced energy saving function would provide the users a superior AC/DC power application of higher efficiency, low external component counts, and lower cost solution for applications.

The SP6878 features more protections or functions for the following characteristics: over voltage protection (OVP); over temperature protection (OTP); over load protection (OLP). SP6878 is available by SOP-8/ DIP-8P packages.

APPLICATIONS

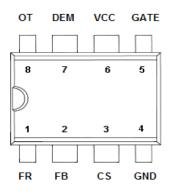
- AC/DC Switching Power Adaptor
- Set-top Box Power Supply
- Open-Frame Switching Power Supply

FEATURES

- High-Voltage BCD Process
- Under Voltage Lockout (UVLO)
- Quasi-Resonant Control
- Internal 4ms Soft Start
- Over Temperature Latch Shutdown
- OLP (Over Load Protection)
- OVP (Over Voltage Protection) on Vcc Pin
- 100KHz Maximum Frequency
- 800mA Driving Capability

PIN CONFIGURATION

SOP-8



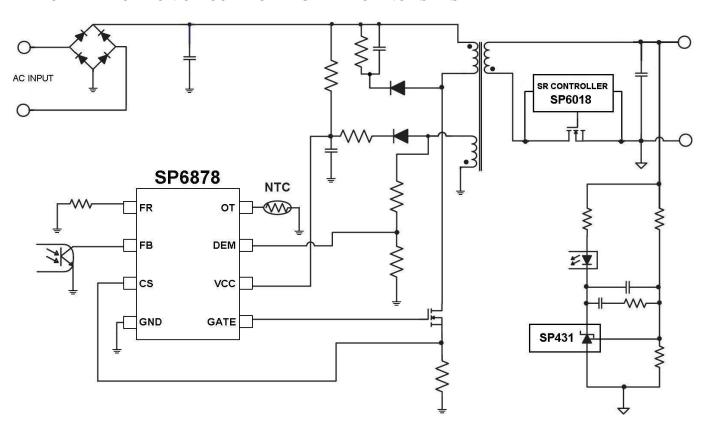
PART MARKING

SOP-8



A:Lot Code B:Date Code

TYPICAL APPLCATION CIRCUIT FOR HIGH EFFICIENCY SMPS



PIN DESCRIPTION

Pin	Symbol	Description		
1	FR	Set the internal frequency and timer.		
2	FB Voltage feedback. It provides feedback to the internal PWM comparator to control the dutcycle.			
3	CS	Current sense.		
4	GND	Ground		
5	GATE	Gate driver output to drive the external MOSFET.		
6	Vcc	Supply voltage for the IC		
7	DEM	Core reset detection and OVP.		
8	OT	Over Temperature Protection by connection through a NTC resistor to GND.		

BLOCK DIAGRAM 1 FR VDD Trimmed GATE Internal reference Voltage/Current 6 Driver 5 UVLO Reference Q Timer, Regulator Qb Logic, Internal supply Fault OT Management 8 OTP FB OLP 2 Valley detector DEM CS Load OVP 3 GND

ORDERING INFORMATION

Part Number		Package	Part Marking		
	SP6878S8RGB	SOP-8	SP6878		

[※] SP6878S8RGB: Tape Reel; Pb − Free; Halogen-Free

ABSOULTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified.)

The following ratings designate persistent limits beyond which damage to the device may occur.

Symbol	Parameter	Value	Unit	
V_{CC}	DC Supply Voltage		25	V
V _{FR/FB/CS} / DEM /OT	FR /FB / CS/DEM/OT Voltage		-0.3 ~ 7.0	V
ESD	Human Body Model		4	KV
	Machine Model		300	V
T_{ope}	Operating Ambient Temperature		-40 ~ 85	°C
$T_{\mathtt{J}}$	Operating Junction Temperature Range		-40 ~ 150	°C
T_{STG}	Storage Temperature Range		-40 ~ 150	°C
T_{LEAD}	Pb-Free Lead Soldering Temperature for 5 sec.		260	°C
$R_{\Theta JC}$	Thermal Resistance Junction – Case (*)	SOP-8 DIP-8	150 90	°C/W

^(*) The power dissipation and thermal resistance are evaluated under copper board mounted with free air conditions.



ELECTRICAL CHARACTERISTICS

 $(T_A=25^{\circ}\text{C}, V_{CC}=16\text{V}, RFR=20\text{K} \text{ Ohm unless otherwise specified.})$

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	e (Vcc Pin)		•		*	•
Istt	Startup Current	Vcc=UVLO-1.5V		5	15	uA
_		$V_{FB} = 3V$		2.0	4.0	mA
Iop	Operating Current	$V_{FB} = 3V$, $C_L = 1$ nf		3.0	5.0	mA
UVLO (off)	Min. Operating Voltage			7.5		V
UVLO (on)	Start Threshold Voltage			13.5		V
OVP Level	Over Voltage Protection			19		V
Vcc Clamp	Clamping Voltage	Ivcc = 5mA		20		V
Voltage Feedba	ack (FB Pin)					
Isc	Short Circuit Current			1.5		mA
Vop	Open Loop Voltage			5.3		V
$V_{TH_BM_on}$	Burst Mode on threshold			0.8		V
VTH_BM_off	Burst Mode off threshold			0.7		V
ZfB	Input Impedance			4		$\mathbf{K}\Omega$
TLOLP	OLP Trip Level			4.4		V
TDOLP	OLP Delay Time (note)			80		mS
Demagnetizati	. /	1	1		1	1
VTH(DEM)	Demagnetization Threshold Voltage			75		mV
VСн	Input Clamp Voltage High			6		V
VCL	Input Clamp Voltage Low			-0.7		V
Tsupp	Suppression of the transformer ringing at start of secondary stoke			2.5		uS
TDEM	Demag Propagation Delay			250		nS
VTH_ OVP	Output OVP trigger point			3.75		V
Current Sensir				3.75		
VTH_						
Duty_zero	Internal current limiting threshold	Zero duty cycle, V _{FB} =3V	0.415	0.45	0.485	V
V _{TH} _ Duty_max	Internal current limiting threshold	Max duty cycle, V _{FB} =3V		0.8		V
.	Burst mode CS threshold	Zero Output, VFB=1V		0.3		V
TLEB	Leading Edge Blanking Time	Zero Gutput, VIB-I V		300		nS
	utput (GATE Pin)			300		115
	Output Low Level	Vcc=15V, Io=100mA			1	V
Vон	Output High Level	Vcc=15V, Io=100mA	7.5			V
VG_Clamp	Output Clamp Voltage Level	Vcc=18V	7.0	16.5		
Tr	Rising Time	CL = 1nf		80		nS
Tf	Falling Time	CL = 1nf		30		nS
Frequency Set		1111			-	110
RFR	Resistor Range			20		$\mathbf{K}\Omega$
V_FR_open	FR open voltage			2.0		V
Fburst	Burst mode switching frequency			22		KHz
Fmax_QR_L	Frequency low clamp in QR mode		47	52	57	KHz
	Frequency high clamp in QR mode		82	90	98	KHZ
G_PFM	PFM mode frequency modulation slope		02	90	70	KHZ
	Fmin_QR_L frequency shuffling range		-4	70	+4	%
Ton	Maximum ON Time	$RFR = 20 \text{ K}\Omega$	10	13	15	uS

Toff	Maximum OFF Time	Rfr = $20 \text{ K}\Omega$	40	55	75	uS	
Over Temp Protection (OT Pin)							
V_OT_open	OT pin open voltage			3.5		V	
VTH(OTP)	OTP Threshold Voltage		1.00	1.05	1.10	V	
Іот	Output Current of OT pin	$RFR = 20K\Omega$		100		uA	
Soft Start							
T_soft	Internal soft startup			4		mS	

Note: The OLP delay time is proportional to the period of switching cycle. So that, the lower FR resistor value will set the higher switching frequency and the shorter OLP delay time.

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