



SPC4567W

N & P Pair Enhancement Mode MOSFET

DESCRIPTION

The SPC4567W is the N- and P-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

FEATURES

N-Channel

- 40V/6.0A, $R_{DS(ON)}=53m\Omega @ V_{GS}=10V$
- 40V/5.0A, $R_{DS(ON)}=63m\Omega @ V_{GS}=4.5V$
- 40V/4.5A, $R_{DS(ON)}=78m\Omega @ V_{GS}=2.5V$

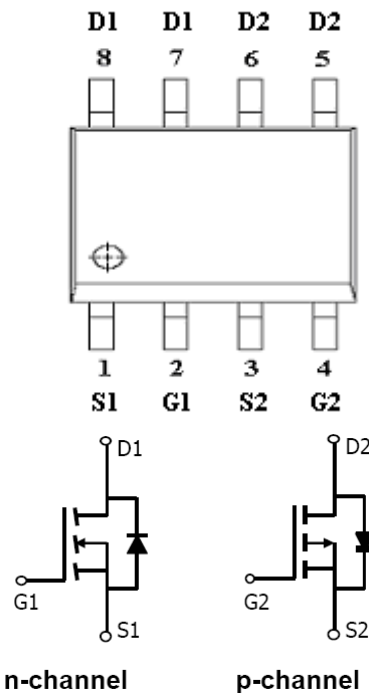
P-Channel

- -40V/-7.2A, $R_{DS(ON)}=95m\Omega @ V_{GS}=-10V$
- -40V/-5.0A, $R_{DS(ON)}=110m\Omega @ V_{GS}=-4.5V$
- Super high density cell design for extremely
- low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOP – 8P package design

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

PIN CONFIGURATION(SOP-8P)



PART MARKING





SPC4567W

N & P Pair Enhancement Mode MOSFET

PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPC4567WS8RGB	SOP- 8P	SPC4567W

※ SPC4567WS8RGB 13" Tape Reel ; Pb – Free ; Halogen – Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical		Unit
		N-Channel	P-Channel	
Drain-Source Voltage	V _{DSS}	40	-40	V
Gate –Source Voltage	V _{GSS}	20	-20	V
Continuous Drain Current(T _J =150°C)	I _D	TA=25°C	-7.2	A
		TA=70°C	-5.0	
Pulsed Drain Current	I _{DM}	25	-25	A
Continuous Source Current(Diode Conduction)	I _S	2.3	-2.3	A
Power Dissipation	P _D	TA=25°C	2.8	W
		TA=70°C	1.8	
Operating Junction Temperature	T _J	-55/150		°C
Storage Temperature Range	T _{STG}	-55/150		°C
Thermal Resistance-Junction to Ambient	R _{θJA}	T ≤ 10sec	52	°C/W
		Steady State	80	



SPC4567W

N & P Pair Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (NMOS)

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.5		1.0	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	uA
		V _{DS} =40V, V _{GS} =0V T _J =85°C			5	
On-State Drain Current	I _{D(on)}	V _{DS} = 5V, V _{GS} =4.5V	10			A
Drain-Source On-Resistance	R _{DSON}	V _{GS} = 10V, I _D =6.0A		42	53	mΩ
		V _{GS} =4.5V, I _D =5.0A		52	63	
		V _{GS} =2.5V, I _D =4.5A		67	78	
Forward Transconductance	g _{fs}	V _{DS} =15V, I _D =6.2A		13		S
Diode Forward Voltage	V _{SD}	I _S =2.3A, V _{GS} =0V		0.8	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =15V, V _{GS} =10V I _D = 2A		16	24	nC
Gate-Source Charge	Q _{gs}			3		
Gate-Drain Charge	Q _{gd}			2.5		
Turn-On Time	t _{d(on)}	V _{DD} =15V, R _L =15Ω I _D =1.0A, V _{GEN} =10V R _G =6Ω		15	20	nS
	t _r			6	12	
Turn-Off Time	t _{d(off)}			10	20	
	t _f			40	80	



SPC4567W

N & P Pair Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (PMOS)

(TA=25°C Unless otherwise noted)

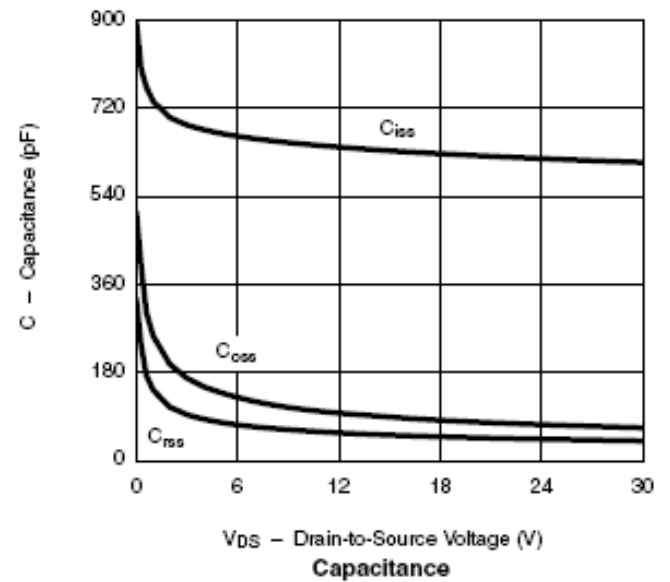
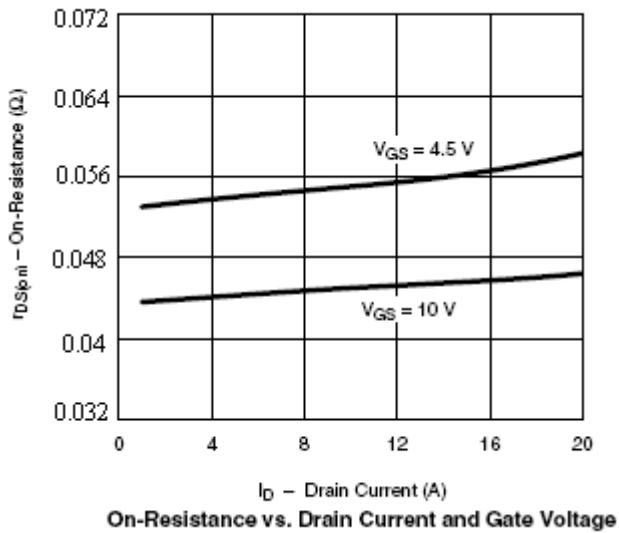
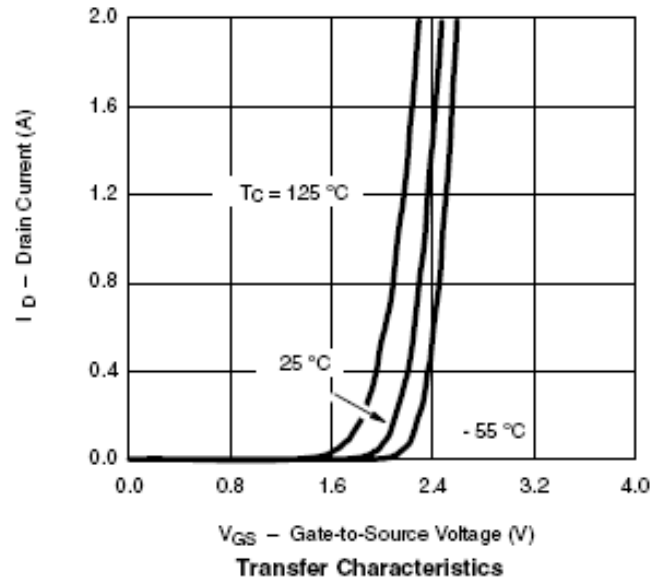
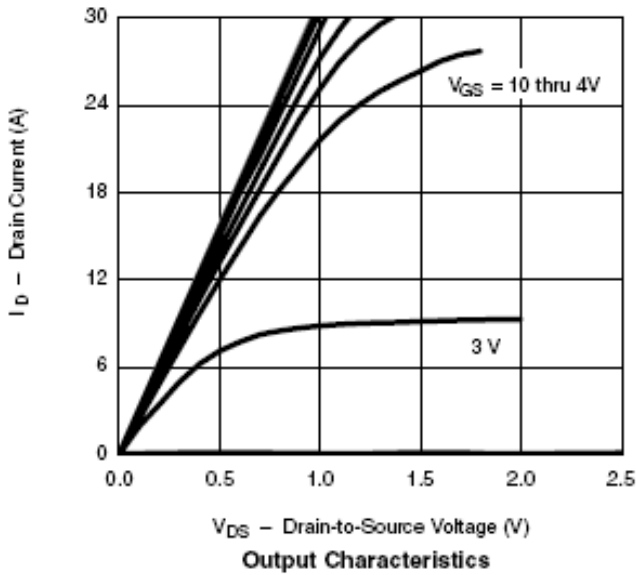
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250uA	-40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.8		-2.5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-36V, V _{GS} =0V			-1	uA
		V _{DS} =-36V, V _{GS} =0V T _J =85°C			-5	
On-State Drain Current	I _{D(on)}	V _{DS} = -5V, V _{GS} =-4.5V	-10			A
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} =-10V, I _D =-7.2A		82	95	mΩ
		V _{GS} =-4.5V, I _D =-5.0A		95	110	
Forward Transconductance	g _{fs}	V _{DS} =-15V, I _D =-5.7A		13		S
Diode Forward Voltage	V _{SD}	I _S =-1.3A, V _{GS} =0V		-0.55	-1.0	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-15V, V _{GS} =-10V I _D = -3.5A		9	12	nC
Gate-Source Charge	Q _{gs}			1.5		
Gate-Drain Charge	Q _{gd}			2.0		
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V f=1MHz		500		pF
Output Capacitance	C _{oss}			95		
Reverse Transfer Capacitance	C _{rss}			50		
Turn-On Time	t _{d(on)}	V _{DD} =-15V, R _L =15Ω I _D =-1.0A, V _{GEN} =-10V R _G =6Ω		8	20	nS
	t _r			10	20	
Turn-Off Time	t _{d(off)}			30	35	
	t _f			15	20	



SPC4567W

N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (NMOS)

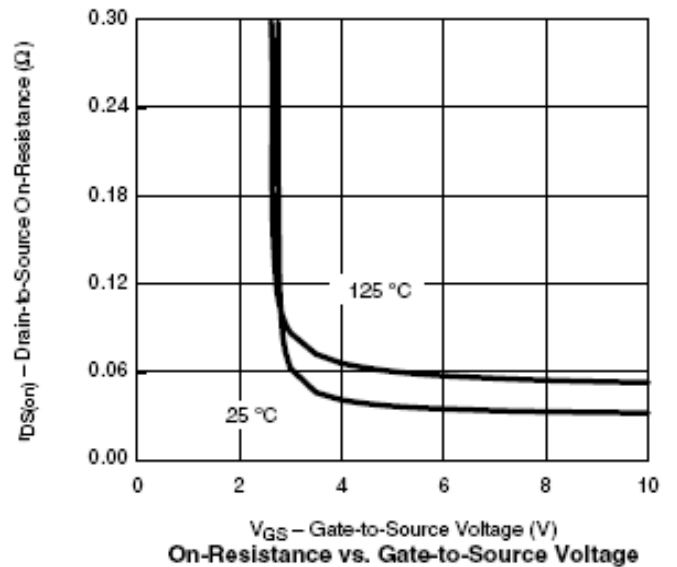
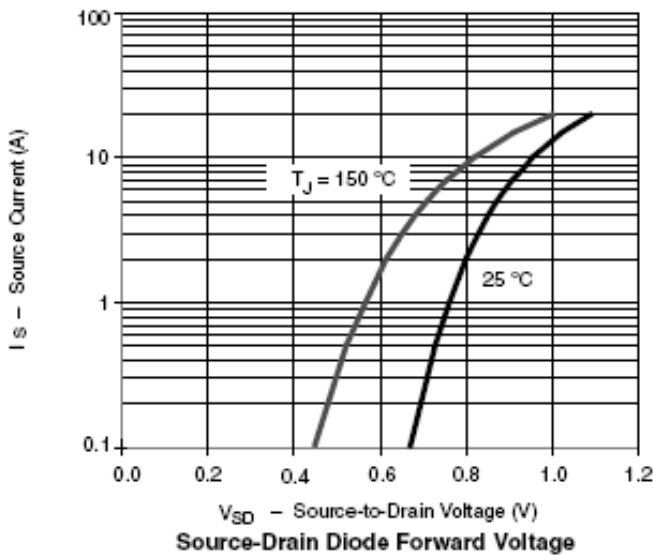
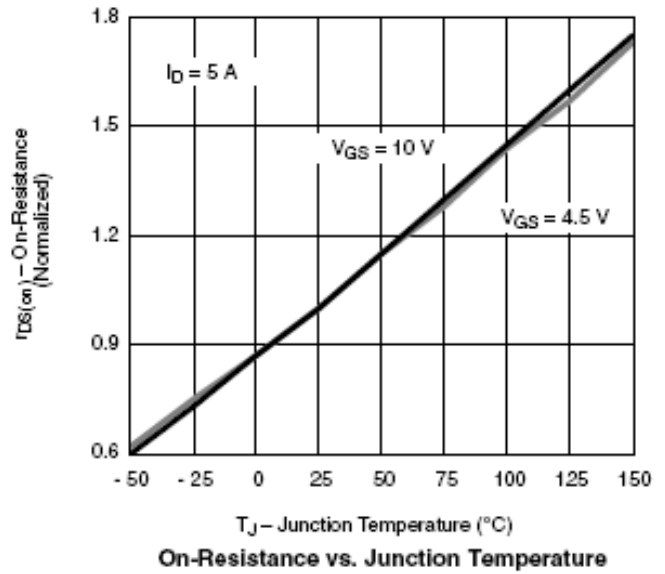
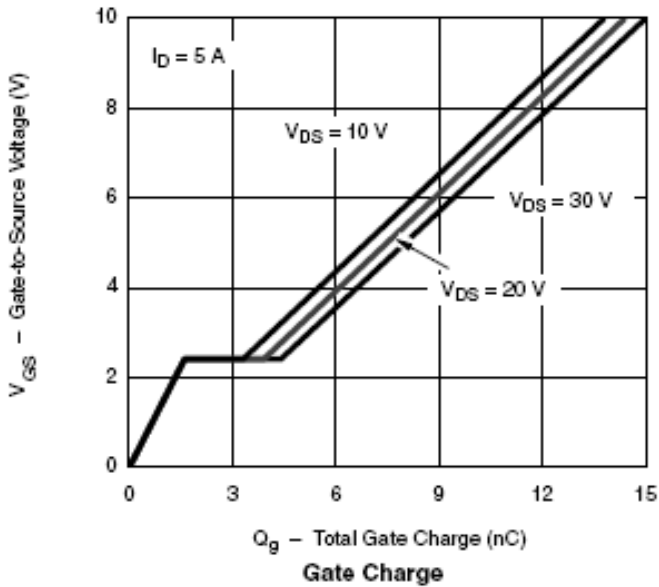




SPC4567W

N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (NMOS)

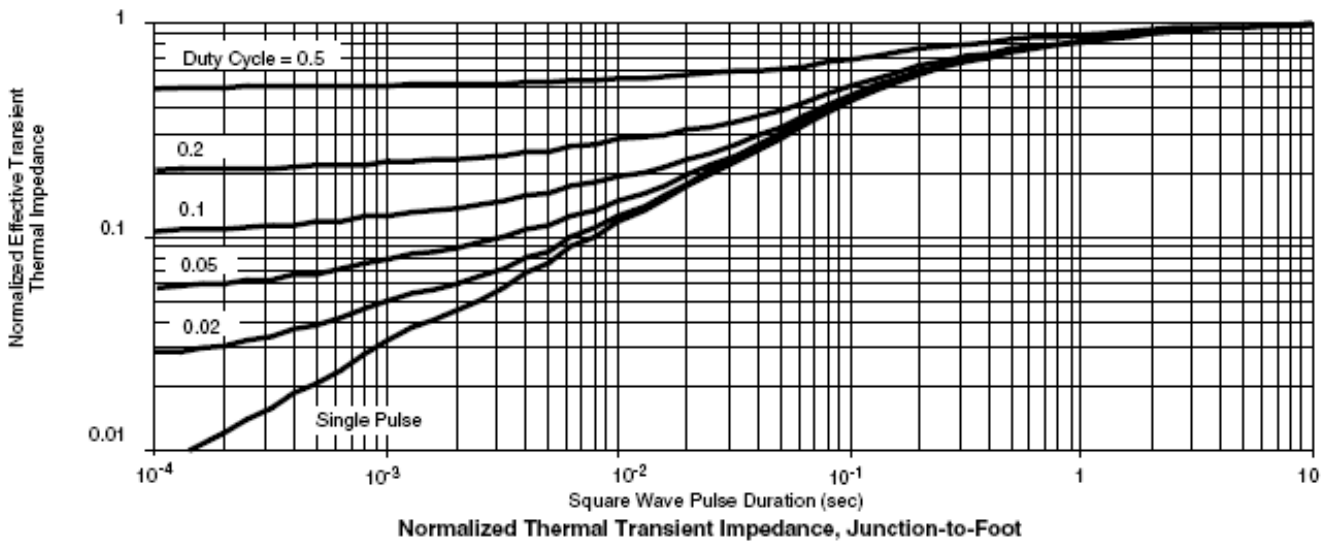
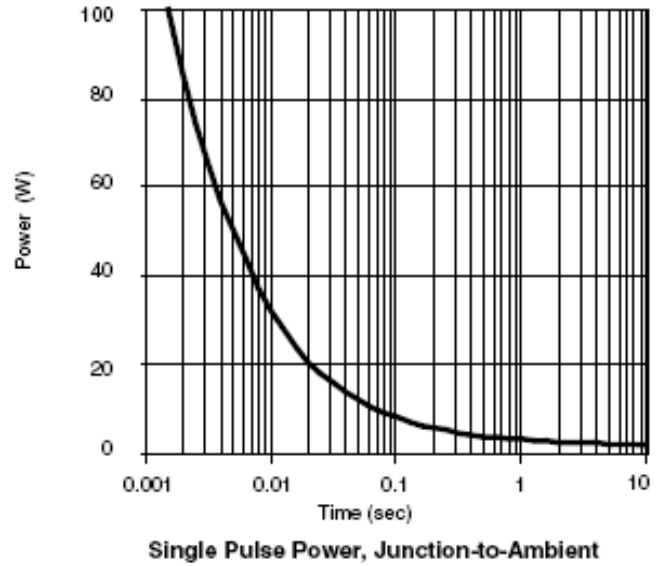
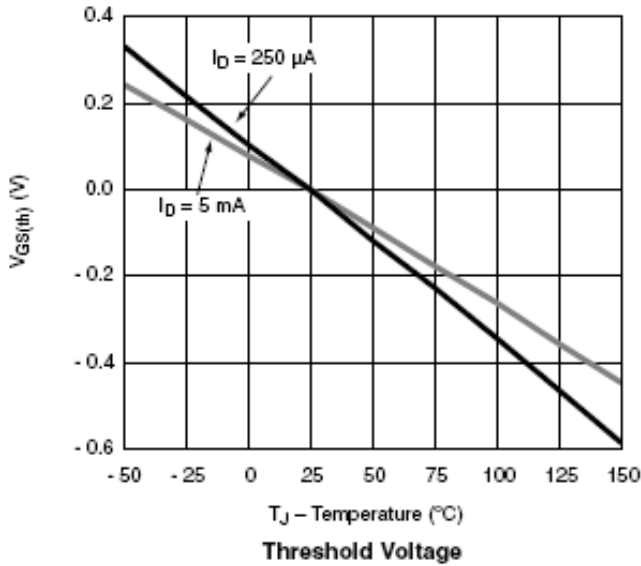




SPC4567W

N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (NMOS)

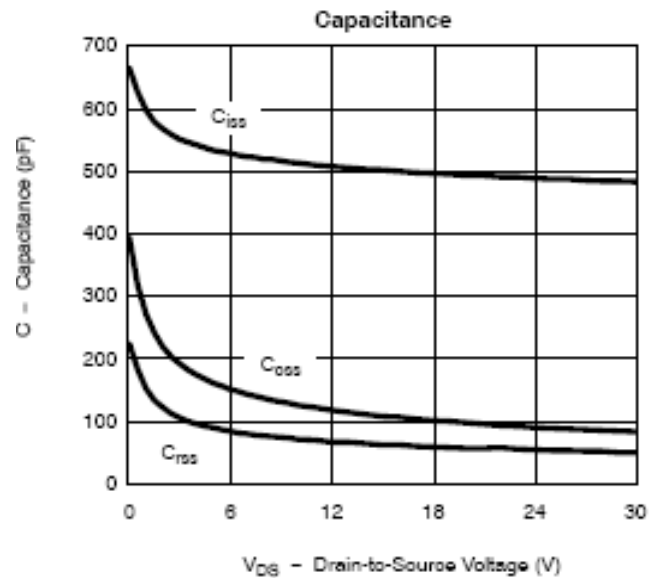
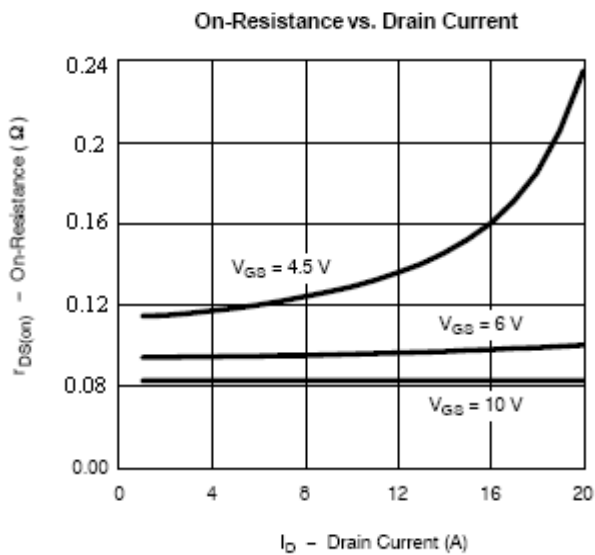
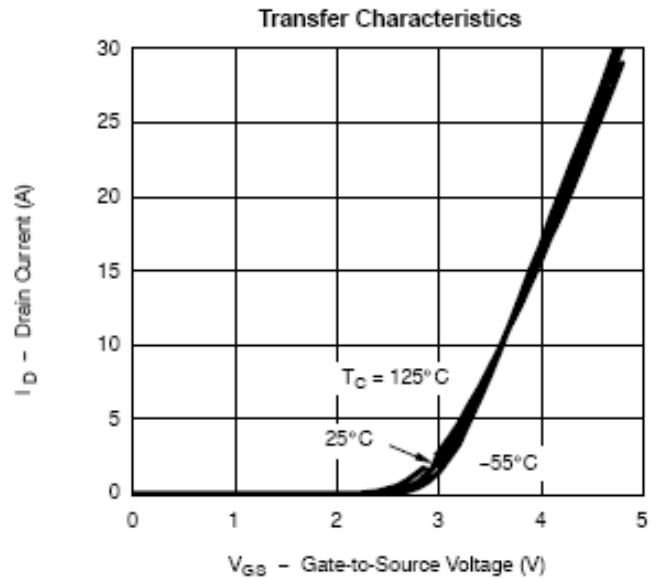
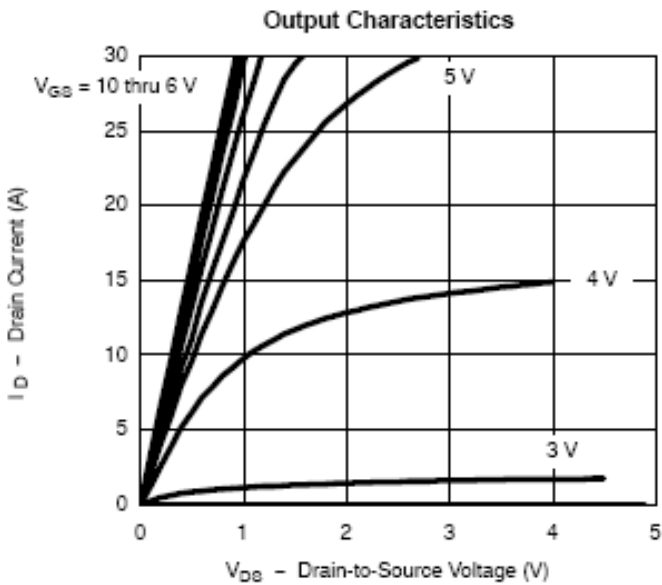




SPC4567W

N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (PMOS)

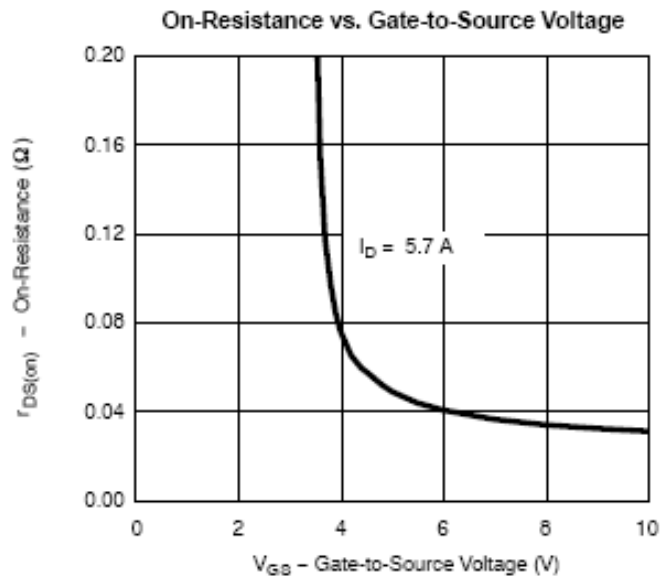
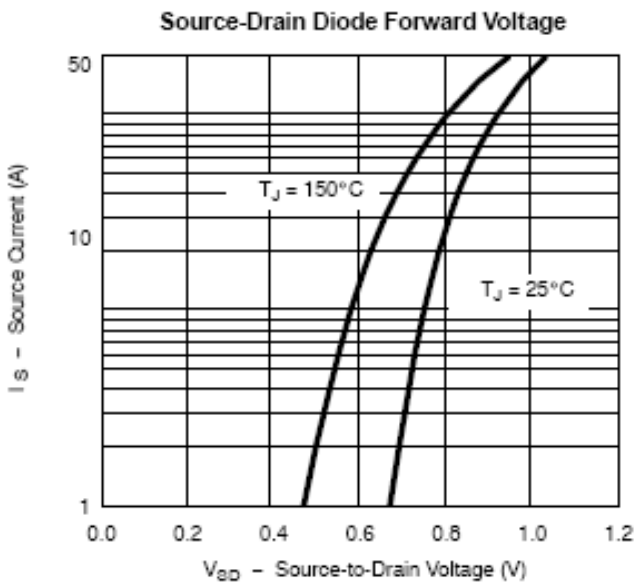
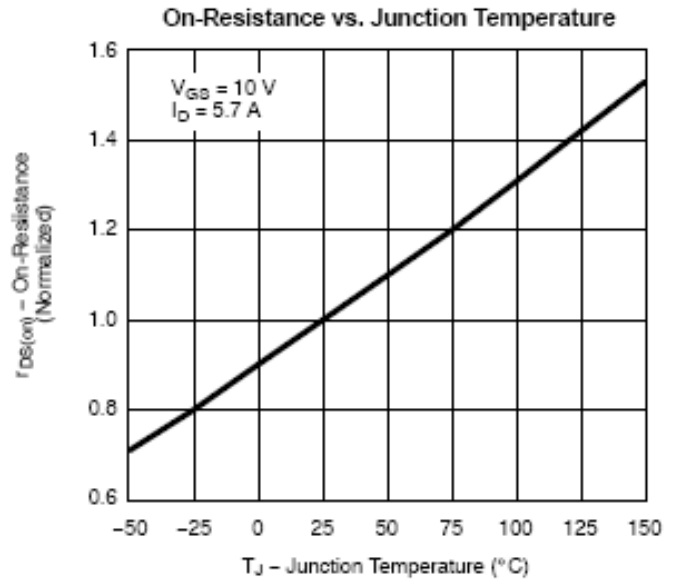
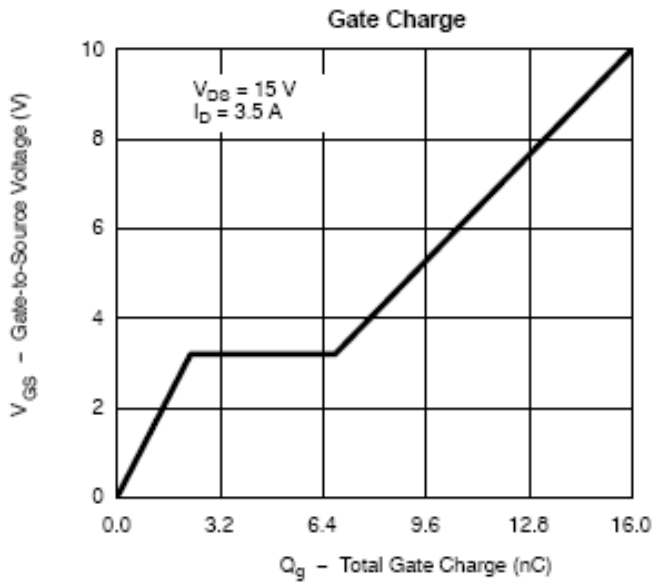




SPC4567W

N & P Pair Enhancement Mode MOSFET

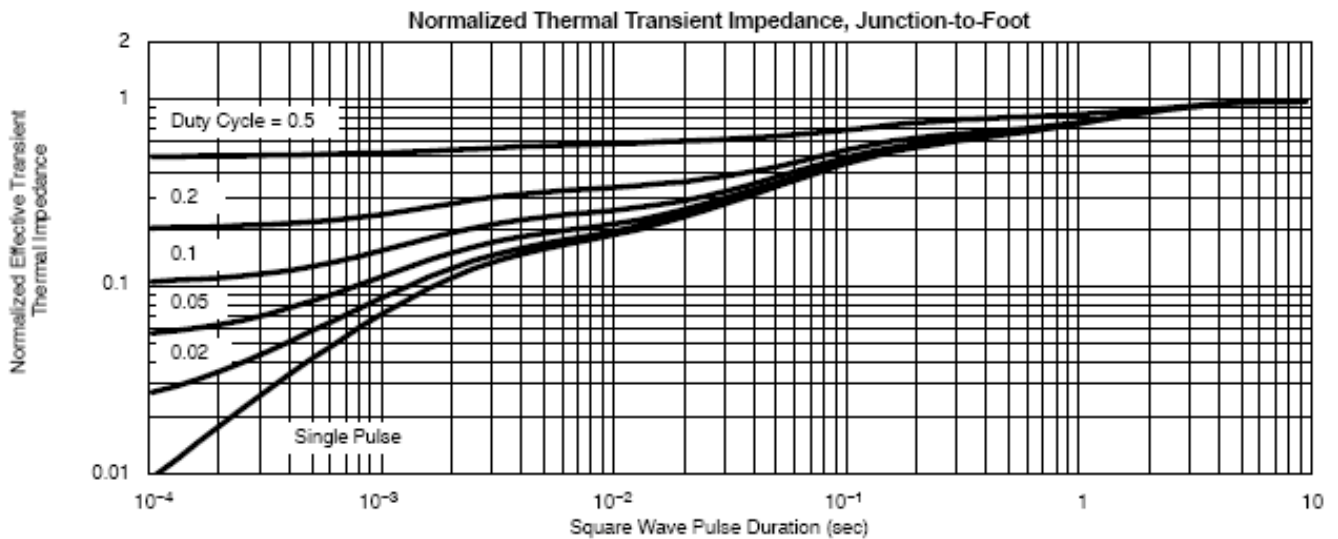
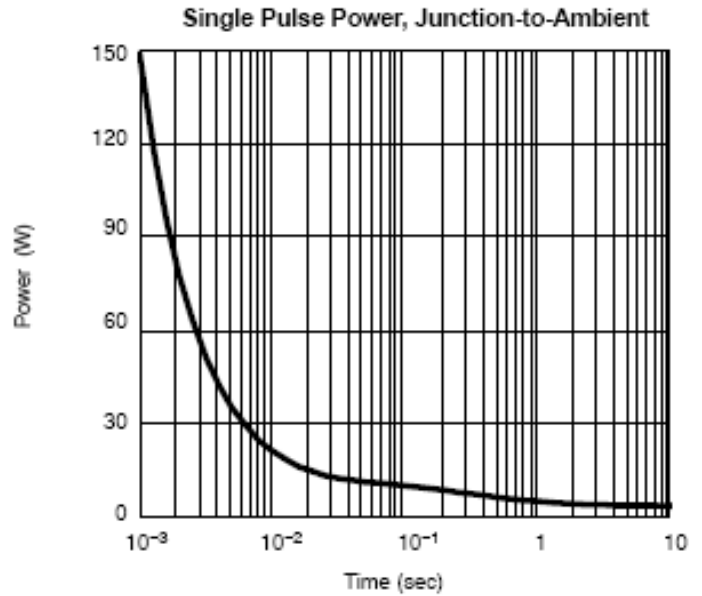
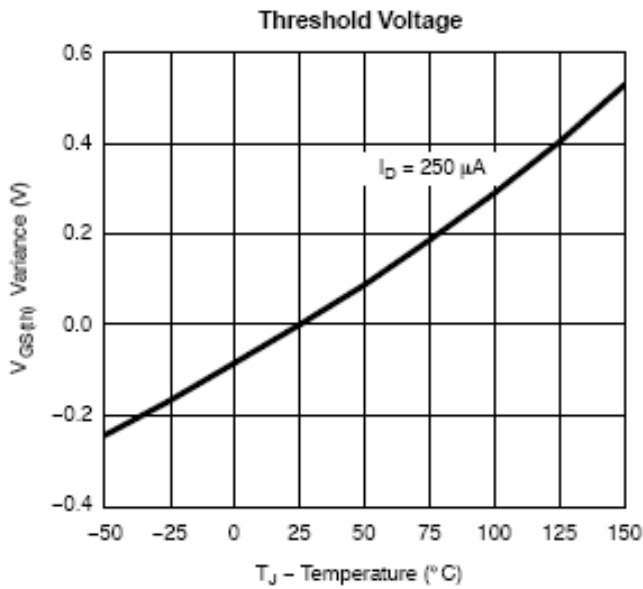
TYPICAL CHARACTERISTICS (PMOS)





SPC4567W N & P Pair Enhancement Mode MOSFET

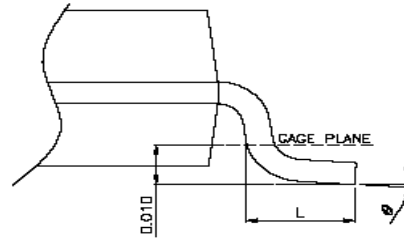
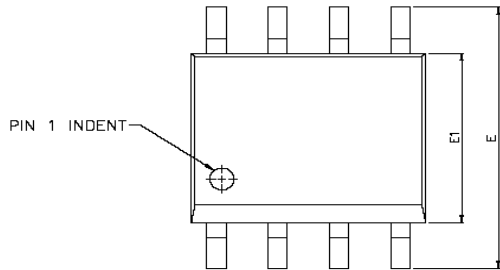
TYPICAL CHARACTERISTICS (PMOS)



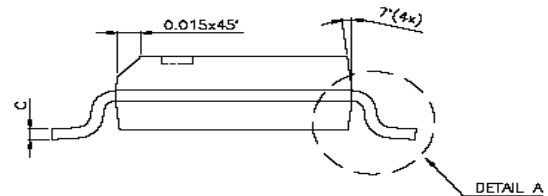
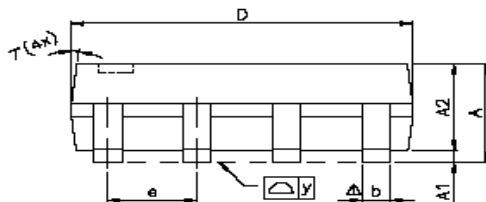


SPC4567W N & P Pair Enhancement Mode MOSFET

SOP- 8 PACKAGE OUTLINE



DETAIL A



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
Δ y	—	—	0.076	—	—	0.003
∅	0°	—	8°	0°	—	8°



SPC4567W

N & P Pair Enhancement Mode MOSFET

Information provided is alleged to be exact and consistent. SYNC Power Corporation presumes no responsibility for the penalties of use of such information or for any violation of patents or other rights of third parties which may result from its use. No license is granted by allegation or otherwise under any patent or patent rights of SYNC Power Corporation. Conditions mentioned in this publication are subject to change without notice. This publication surpasses and replaces all information previously supplied. SYNC Power Corporation products are not authorized for use as critical components in life support devices or systems without express written approval of SYNC Power Corporation.

© The SYNC Power logo is a registered trademark of SYNC Power Corporation

© 2004 SYNC Power Corporation – Printed in Taiwan – All Rights Reserved

SYNC Power Corporation

7F-2, No.3-1, Park Street

NanKang District (NKSP), Taipei, Taiwan 115

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

© <http://www.syncpower.com>