



SPE0521

Single-Line ESD Protection Array

DESCRIPTION

The SPE0521 are designed by TVS bi-direction device that is to protect sensitive electronics from damage or latch-up due to ESD. They are designed for use in applications where board space is at a premium. SPE0521 will protect single line, and may be used on line where the signal polarities swing above and below ground.

SPE0521 offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

SPE0521 may be used to meet the immunity requirements of IEC 61000-4-2, level 4. The small SOD-523 package makes them ideal for use in portable electronics such as cell phones, PDA's, notebook computers, and digital cameras.

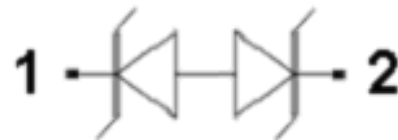
APPLICATIONS

- ◆ Cellular Handsets and Accessories
- ◆ Cordless Phone
- ◆ PDA
- ◆ Notebooks and Handhelds
- ◆ Portable Instrumentation
- ◆ Digital Cameras
- ◆ MP3 Player

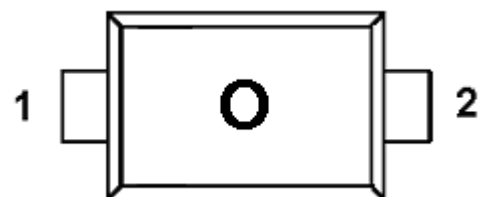
FEATURES

- ◆ Transient protection for data lines to IEC 61000-4-2 (ESD) $\pm 15\text{kV}$ (air), $\pm 8\text{kV}$ (contact)
IEC 61000-4-4 (EFT) 40A (5/50ns)
- ◆ Protects single I/O lines
- ◆ Working voltage: 5V
- ◆ Low leakage current
- ◆ Low operating and clamping voltages

PIN CONFIGURATION (SOD-523)



PART MARKING





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ORDERING INFORMATION

| Part Number | Package | Part Marking |
|---------------|---------|--------------|
| SPE0521D52RGB | SOD-523 | O |

※ SPE0521D52RGB : Tape Reel ; Pb – Free; Halogen – Free

※ Date code format on label : YW

Y: year code (ext. 2011 : 1; 2012: 2)

W: week code (A~Z, a~z,@)

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

| Parameter | Symbol | Typical | Unit |
|---|--------|---------------|------|
| Peak Pulse Power (tp = 8/20 μs) | Ppk | 250 | W |
| Maximum Peak Pulse Current (tp = 8/20 μs) | Ipp | 7 | A |
| ESD per ICE 61000 – 4 – 2 (Air) | Vpp | ±15 | KV |
| ESD per ICE 61000 – 4 – 2 (Contact) | Vpp | ±8 | KV |
| Operating Junction Temperature | Tj | -55 ~ 150 | °C |
| Storage Temperature Range | TSTG | -55 ~ 150 | °C |
| Lead Soldering Temperature | TL | 260 (10sec) | °C |

ELECTRICAL CHARACTERISTICS

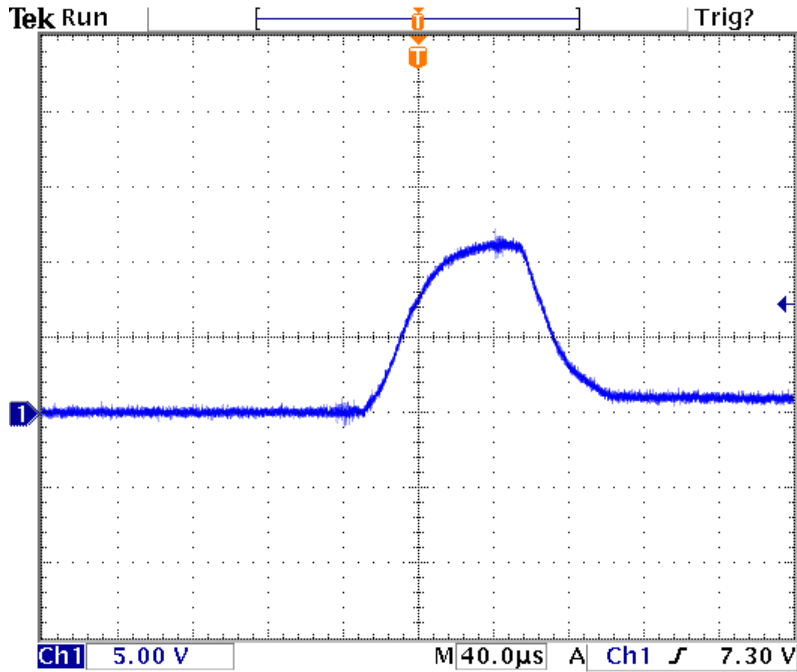
(TA=25°C Unless otherwise noted)

| Parameter | Symbol | Conditions | Min. | Typ | Max. | Unit |
|-----------------------------|--------|---|------|------|------|------|
| Reverse Stand – Off Voltage | VRWM | | | | 5 | V |
| Reverse Breakdown Voltage | VBR | It = 1mA | 6 | | | V |
| Reverse Leakage Current | IR | VRWM = 5V , T=25°C | | 0.01 | 1 | μA |
| Reverse Leakage Current | IR | VRWM = 3V , T=25°C | | 0.01 | 0.5 | μA |
| Clamping Voltage | Vc | Ipp = 1A , tp = 8/20 μs | | | 13 | V |
| Clamping Voltage | Vc | Ipp = 7A , tp = 8/20 μs | | | 15 | V |
| Junction Capacitance | Cj | Between I/O Pin and GND VR = 0V , f = 1MHz | | 5 | 10 | pF |



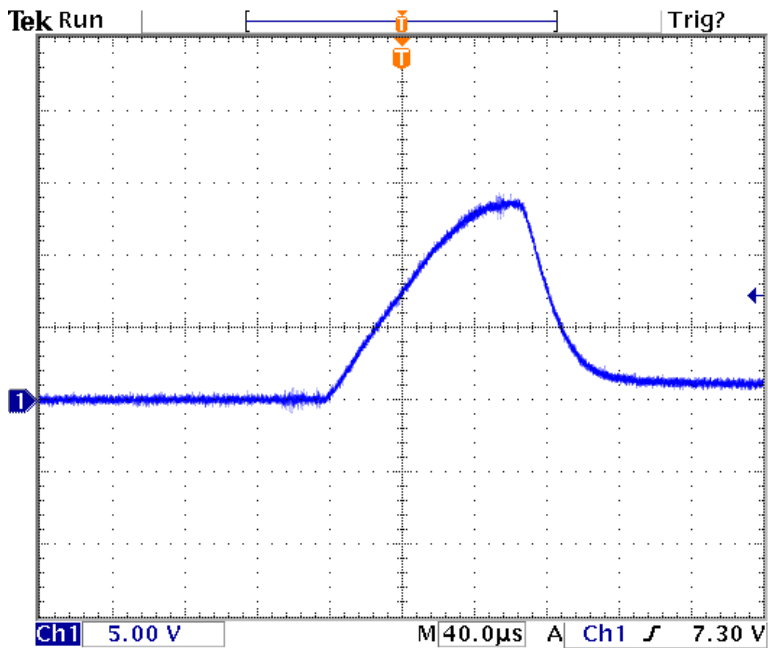
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TYPICAL CHARACTERISTICS



26 Oct 2005
12:15:46

Clamping Voltage ($I_{pp} = 1A$, $t_p = 8/20 \mu s$)



26 Oct 2005
12:08:20

Clamping Voltage ($I_{pp} = 7A$, $t_p = 8/20 \mu s$)



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TYPICAL CHARACTERISTICS

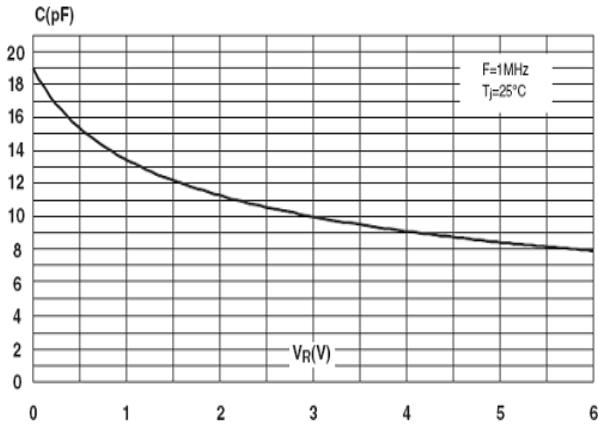


Fig 1 : Junction Capacitance V.S Reverse Voltage Applied

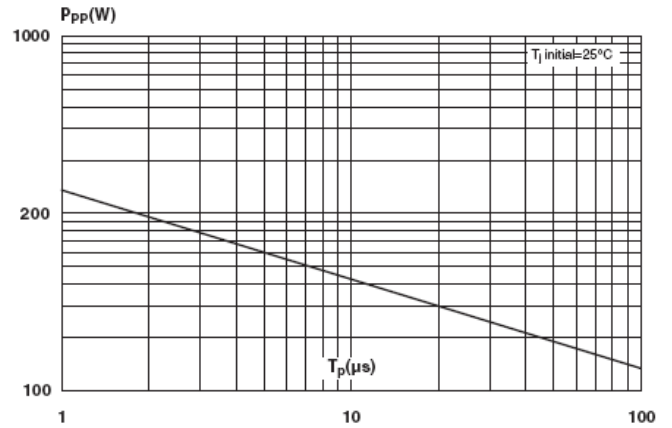


Fig 2 : Peak Plus Power V.S Exponential Plus Duration

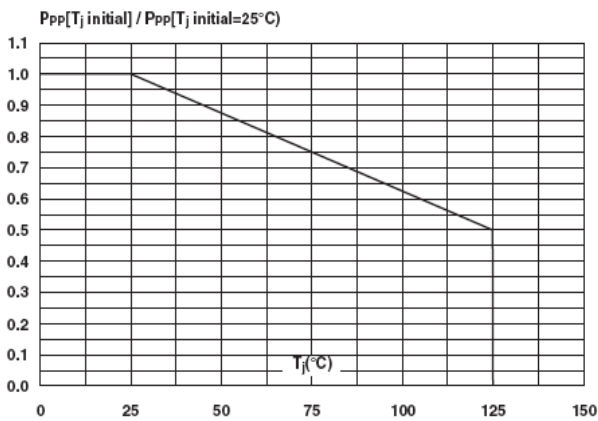


Fig 3 : Relative Variation of Peak Plus Power V.S Initial Junction Temperature

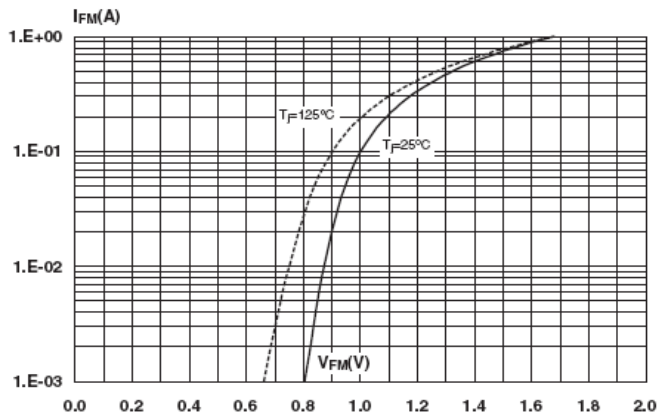


Fig 4 : Forward Voltage Drop V.S Peak Forward Current



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SYNC Power Corporation

7F-2, No.3-1, Park Street

NanKang District (NKSP), Taipei, Taiwan, 115, R.O.C

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

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