

DESCRIPTION

The SPN1302 is the N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, and low in-line power loss, and resistance to transients are needed.

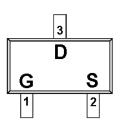
APPLICATIONS

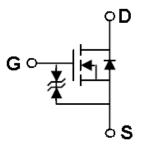
- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
 - DSC
- LCD Display inverter

FEATURES

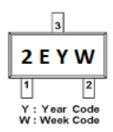
- N-Channel 20V/0.65A,RDS(ON)= $380m\Omega@V$ GS=4.5V 20V/0.55A,RDS(ON)= $450m\Omega@V$ GS=2.5V 20V/0.45A,RDS(ON)= $800m\Omega@V$ GS=1.8V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ♦ ESD protected
- ◆ SOT-323 (SC-70) package design

PIN CONFIGURATION (SOT-323 / SC-70)





PART MARKING



PIN DESCRIPTION						
Pin	Symbol	Description				
1	G	Gate				
2	S	Source				
3	D	Drain				

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN1302S32RGB	SOT-323	2E

[※] SPN1302S32RGB : Tape Reel ; Pb − Free, Halogen - Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter		Symbol	Typical	Unit	
Drain-Source Voltage		Vdss	20	V	
Gate –Source Voltage		VGSS	±12	V	
Continuous Drain Current(T _J =150°C)	Ta=25°C	In	2.0	Δ.	
	Ta=70°C	ID	1.5	A	
Pulsed Drain Current		IDM	10	A	
Continuous Source Current(Diode Conduction)		Is	1.6	A	
Power Dissipation	Ta=25°C	Dr	1.25	W	
	Ta=70°C	PD	0.8	vv	
Operating Junction Temperature		TJ	-55/150	°C	
Storage Temperature Range		Tstg	-55/150	°C	
Thermal Resistance-Junction to Ambient		RθJA	105	°C/W	

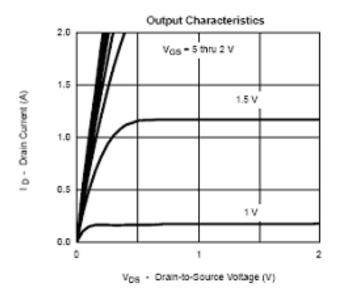


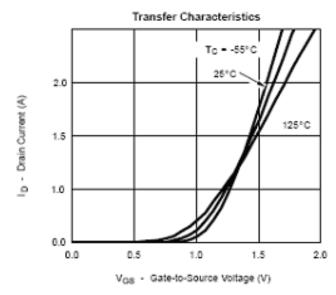
ELECTRICAL CHARACTERISTICS

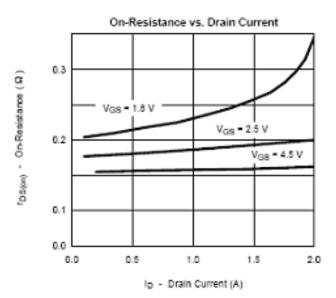
(Ta=25°C Unless otherwise noted)

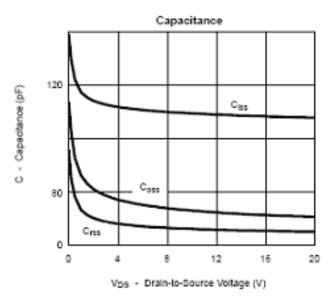
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit
Static			•			
Drain-Source Breakdown Voltage	V(BR)DSS	Vgs=0V,Id=250uA	20			V
Gate Threshold Voltage	VGS(th)	VDS=VGS,ID=250uA	0.35		1.0] '
Gate Leakage Current	Igss	VDS=0V,VGS=±12V			10	uA
Zero Gate Voltage Drain Current	IDSS	VDS=20V,VGS=0V VDS=20V,VGS=0V TJ=55°C			5	uA
On-State Drain Current	ID(on)	V _{DS} ≥4.5V,V _{GS} =5V	0.7			A
Drain-Source On-Resistance	RDS(on)	V _{GS} =4.5V,I _D =0.65A V _{GS} =2.5V,I _D =0.55A V _{GS} =1.8V,I _D =0.45A		0.26 0.32 0.42	0.38 0.45 0.80	Ω
Forward Transconductance	gfs	VDS=10V,ID=0.4A		1.0		S
Diode Forward Voltage	Vsd	Is=0.15A,VGS=0V		0.8	1.2	V
Dynamic			•			
Total Gate Charge	Qg	V _{DS} =10V,V _{GS} =4.5V,		1.2	1.5	nC
Gate-Source Charge	Qgs	ID=0.6A		0.2		
Gate-Drain Charge	Qgd			0.3		
Input Capacitance	Ciss			110		pF
Output Capacitance	Coss	VDS=16V,f=1MHz,		60		
Reverse Transfer Capacitance	Crss	V _{GS} =0V		38		
Turn-On Time	td(on)	$V_{DD}=10V,RL=10\Omega$,		5	10	nS
	tr	ID=0.5A		8	15	
Turn-Off Time	td(off)	$V_{GEN}=4.5V,R_{G}=6\Omega$		10	18	
	tf			1.2	2.8	

TYPICAL CHARACTERISTICS

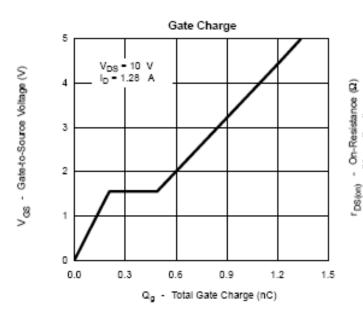


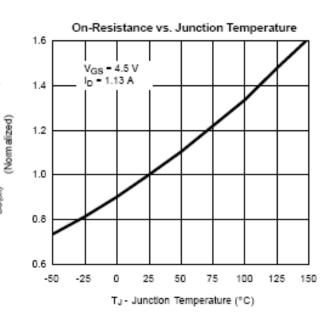


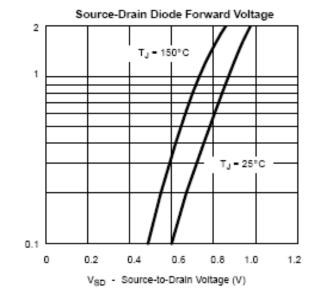




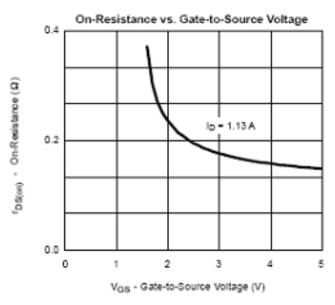
TYPICAL CHARACTERISTICS



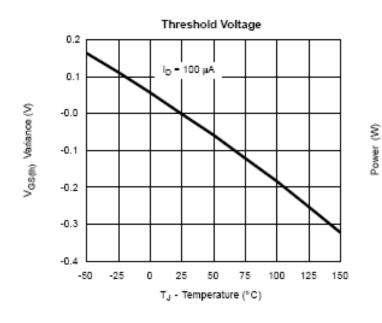


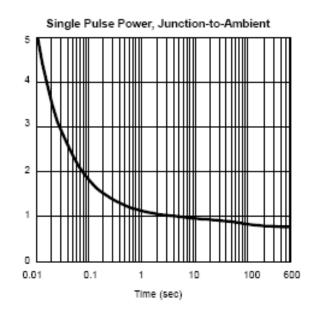


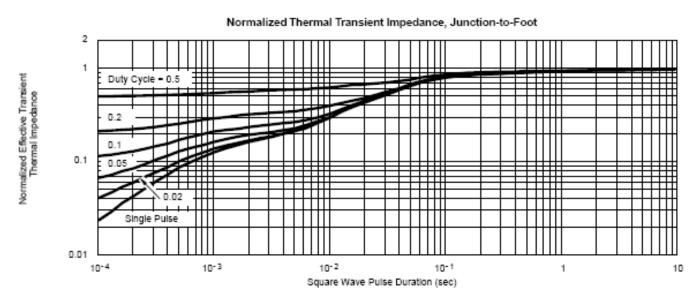
Is - Source Current (A)



TYPICAL CHARACTERISTICS







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