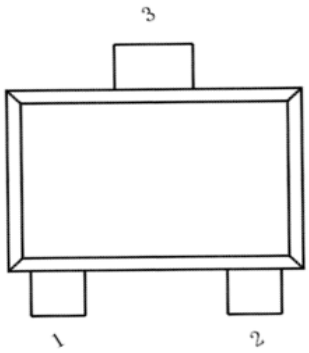
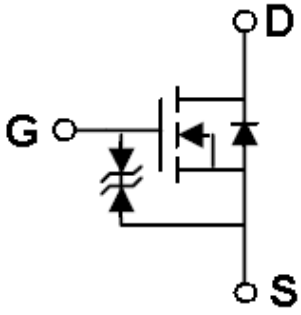
SPN1308W
N-Channel Enhancement Mode MOSFET

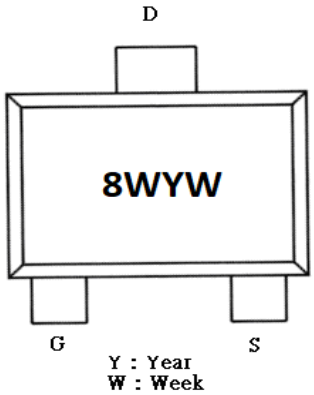
DESCRIPTION
The SPN1308W is the N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

APPLICATIONS
- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

FEATURES
- N-Channel
  30V/1.4A,$R_{DS(ON)}$=190mΩ@V_{GS}=10V
  30V/1.0A,$R_{DS(ON)}$=200mΩ@V_{GS}=4.5V
  30V/0.5A,$R_{DS(ON)}$=250mΩ@V_{GS}=2.5V
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- ESD protected
- SOT-323 package design

PIN CONFIGURATION(SOT-323)




PART MARKING


SPN1308W
N-Channel Enhancement Mode MOSFET

PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN1308WS32RGB	SOT-323	8W

※ SPN1308WS32RGB : Tape Reel ; Pb – Free ; Halogen – Free ; 3K/Reel

ABSOULTE MAXIMUM RATINGS

(T_A=25℃ Unless otherwise noted)

Parameter		Symbol	Typical	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate –Source Voltage		V_{GSS}	±12	V
Continuous Drain Current(T_J=150℃)	T_A=25℃	I_D	1.4	A
Pulsed Drain Current		I_{DM}	6	A
Continuous Source Current(Diode Conduction)		I_S	0.3	A
Power Dissipation	T_A=25℃	P_D	0.33	W
Operating Junction Temperature		T_J	-55/150	℃
Storage Temperature Range		T_{STG}	-55/150	℃
Thermal Resistance-Junction to Ambient		$R_{\theta JA}$	100	℃/W

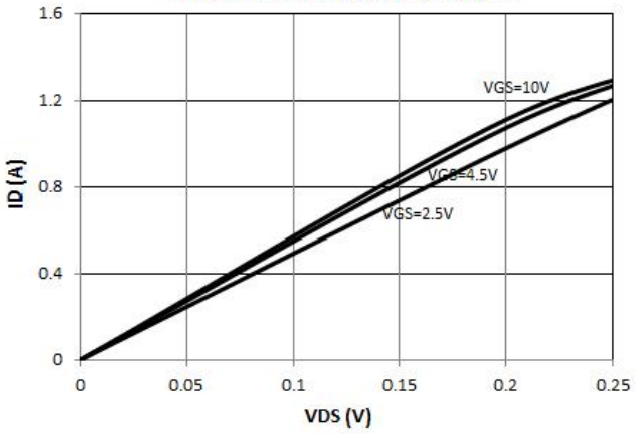
ELECTRICAL CHARACTERISTICS

(T_A=25℃ unless otherwise noted)

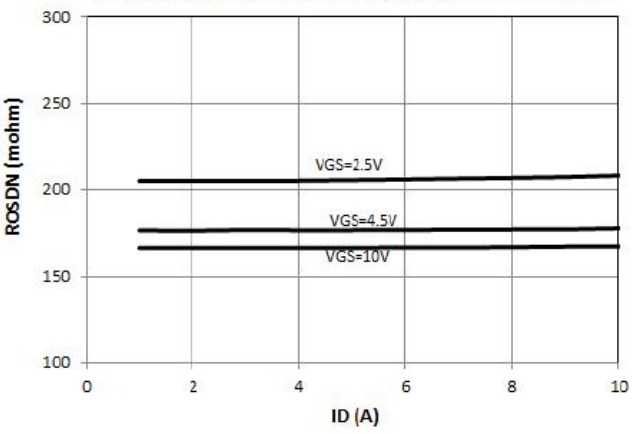
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V,I_D= 250uA	30			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250uA	0.4		1.0	
Gate Leakage Current	I_{GSS}	V_{DS}=0V,V_{GS}=±10V			±10	uA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=24V,V_{GS}=0V			1	uA
		V_{DS}=24V,V_{GS}=0V T_J=55℃			10	
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=1.4A			190	mΩ
		V_{GS}=4.5V,I_D=1A			200	
		V_{GS}=2.5V,I_D=0.5A			250	
Diode Forward Voltage	V_{SD}	I_S=1.4A,V_{GS}=0V			1.2	V
Dynamic						
Total Gate Charge	Q_g	V_{DS}=15V,V_{GS}=4.5V, I_D=1.4A		1.4		nC
Gate-Source Charge	Q_{gs}			0.3		
Gate-Drain Charge	Q_{gd}			0.5		
Input Capacitance	C_{iss}	V_{DS}=15V_{GS}=0V f=1MHz		124		pF
Output Capacitance	C_{oss}			21		
Reverse Transfer Capacitance	C_{rss}			10		
Turn-On Time	$t_{d(on)}$	V_{DD}=15V,R_L=4.4Ω , I_D=1.4A V_{GEN}=10V,R_G=6Ω		2.1		nS
	t_r			9.0		
Turn-Off Time	$t_{d(off)}$			8.5		
	t_f			8.3		

TYPICAL CHARACTERISTICS

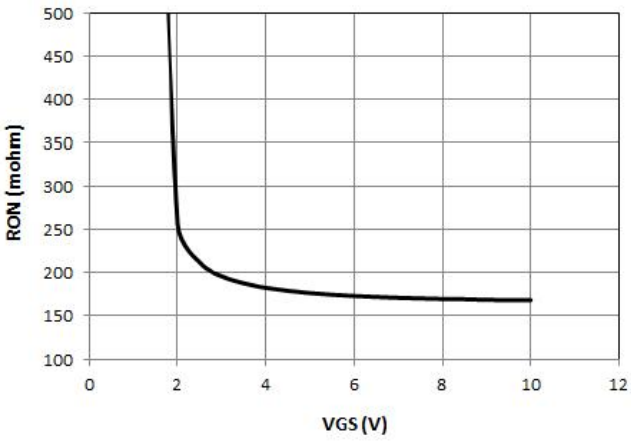
TYPICAL OUTPUT CHARACTERISTICS



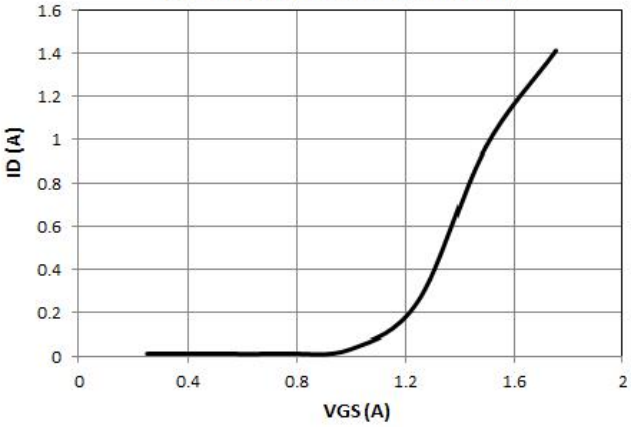
ON-RESISTANCE VS. DRAIN CURRENT ANDGATE VOLTAGE



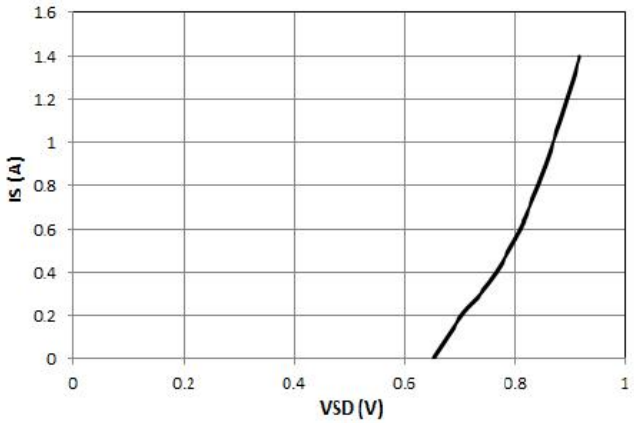
ON-RESISTANCE VS. GATE-SOURCE VOLTAGE



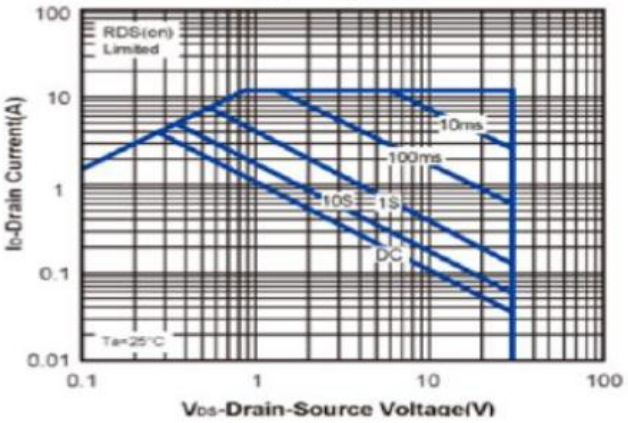
TYPICAL TRANSFER CHARACTERISTICS



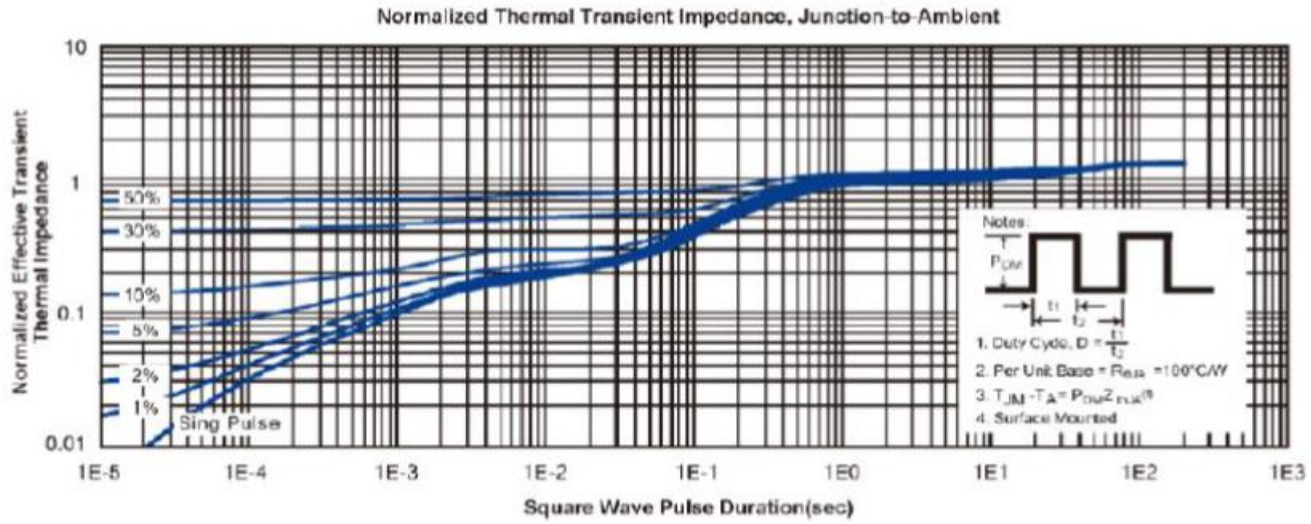
TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE



Maximum Forward Biased Safe Operating Area



TYPICAL CHARACTERISTICS



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