



# SPN2326

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN2326 is the N-Channel enhancement mode power field effect transistor which is produced using super high cell density DMOS trench technology. The SPN2326 has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low  $R_{DS(ON)}$  and fast switching speed.

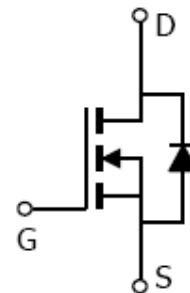
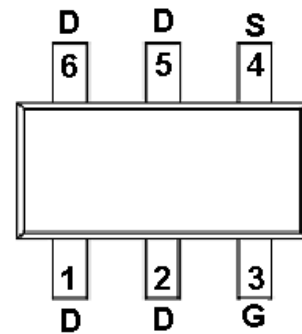
### APPLICATIONS

- Powered System
- DC/DC Converter
- Load Switch

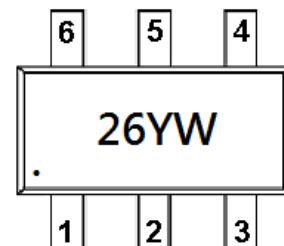
### FEATURES

- ◆ 100V/3A,  $R_{DS(ON)}=310m\Omega@V_{GS}=10V$
- ◆ High density cell design for extremely low  $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-23-6L package design

### PIN CONFIGURATION(SOT-23-6L)



### PART MARKING





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### PIN DESCRIPTION

Pin	Symbol	Description
1	D	Drain
2	D	Drain
3	G	Gate
4	S	Source
5	D	Drain
6	D	Drain

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN2326S26RGB	SOT-23-6L	26

※ SPN2326S26RGB : Tape Reel ; Pb – Free ; Halogen – Free

### ABSOLUTE MAXIMUM RATINGS (TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	100	V	
Gate –Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	TA=25°C	3.0	A
		TA=70°C	2.0	
Pulsed Drain Current	I <sub>DM</sub>	10	A	
Power Dissipation	P <sub>D</sub>	TA=25°C	2.0	W
		TA=70°C	1.3	
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C	
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	62.5	°C/W	



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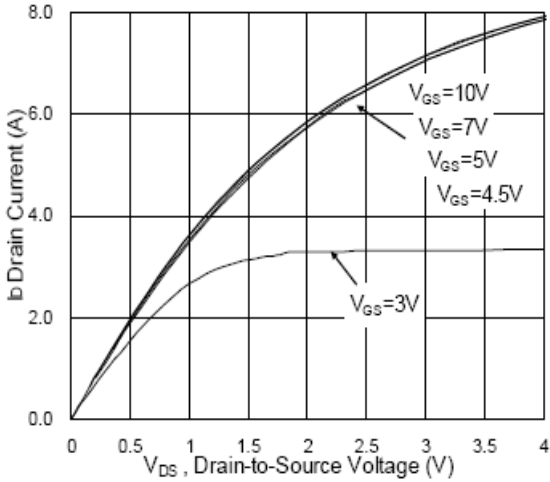
### ELECTRICAL CHARACTERISTICS (TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	2.0	2.5	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=80V, V_{GS}=0V$			1	uA
		$V_{DS}=80V, V_{GS}=0V$ $T_J=125^\circ C$			5	
On-State Drain Current	$I_{D(on)}$	$V_{DS}\geq 5V, V_{GS}=10V$	3.0			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A$		0.26	0.31	$\Omega$
Forward Transconductance	$g_{fs}$	$V_{DS}=10V, I_D=3A$		2.4		S
Diode Forward Voltage	$V_{SD}$	$I_S=1A, V_{GS}=0V$			1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=80V, V_{GS}=10V$ $I_D=5A$		9	13	nC
Gate-Source Charge	$Q_{gs}$			2		
Gate-Drain Charge	$Q_{gd}$			1.4		
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V$ $f=1MHz$		508		pF
Output Capacitance	$C_{oss}$			29		
Reverse Transfer Capacitance	$C_{rss}$			16.5		
Turn-On Time	$t_{d(on)}$	$V_{DD}=50V, R_L=10\Omega$ $I_D=3A, V_{GEN}=10V$ $R_G=3.3\Omega$		2		nS
	$t_r$			21.5		
Turn-Off Time	$t_{d(off)}$			11.2		
	$t_f$			18.8		

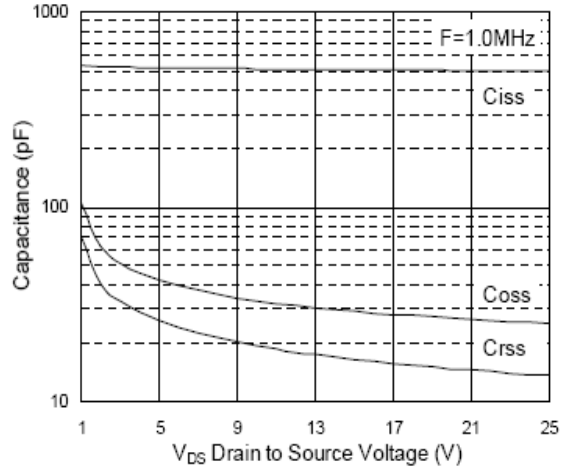


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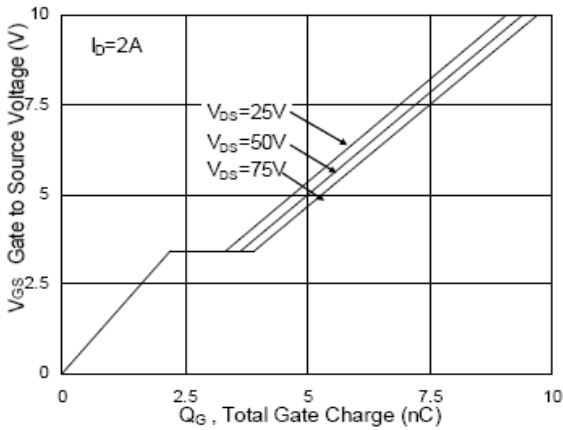
## TYPICAL CHARACTERISTICS



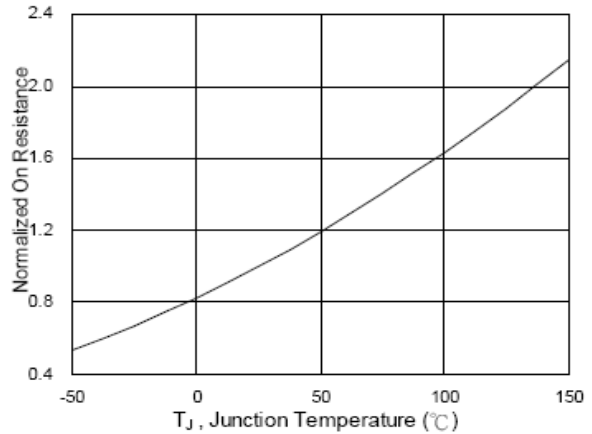
Output Characteristics



Capacitance



Gate Charge



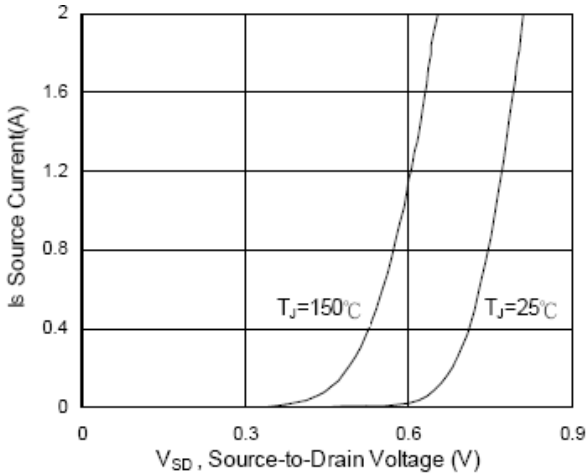
On-Resistance vs. Junction Temperature



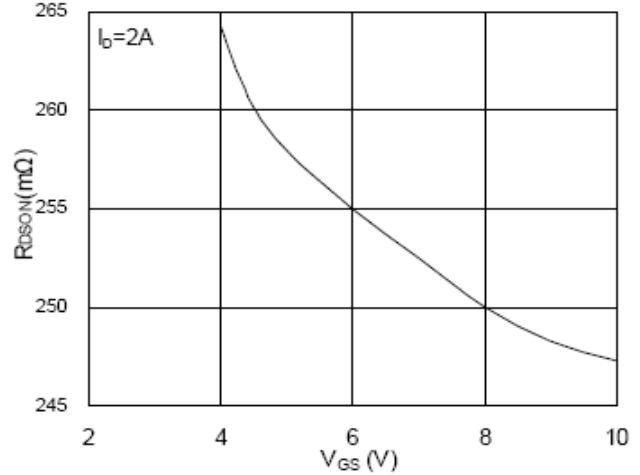
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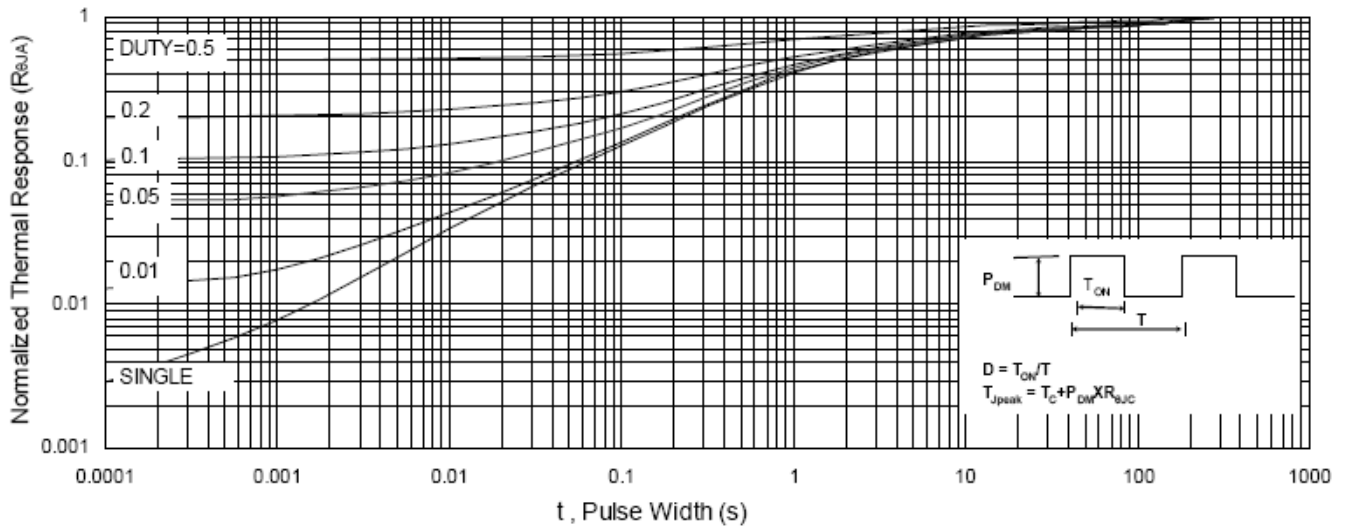
### TYPICAL CHARACTERISTICS



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-Source Voltage



Normalized Thermal Transient Impedance, Junction to Foot



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