



# SPN3632

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN3632 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

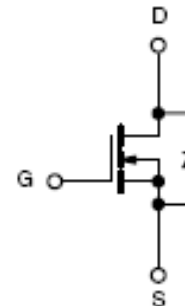
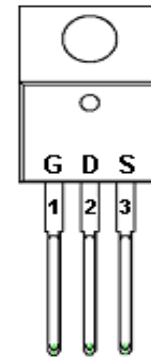
### FEATURES

- ◆ 100V/80A,  $R_{DS(ON)} = 8.8m\Omega @ V_{GS} = 10V$
- ◆ 100V/40A,  $R_{DS(ON)} = 13m\Omega @ V_{GS} = 6.0V$
- ◆ 100V/10A,  $R_{DS(ON)} = 10m\Omega @ V_{GS} = 4.5V$
- ◆ Super high density cell design for extremely low  $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L package design

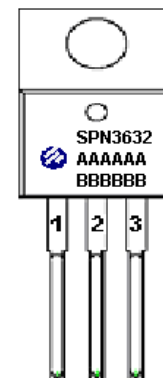
### APPLICATIONS

- DC/DC Converter
- Load Switch
- SMPS Secondary Side Synchronous Rectifier

### PIN CONFIGURATION( TO-220-3L )



### PART MARKING



A : Lot Code  
 B : Date Code  
 (YY / MM / DD)



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### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN3632T220TGB	TO-220-3L	SPN3632

※ SPN3632T220TGB: Tube ; Pb – Free; Halogen – Free

### ABSOLUTE MAXIMUM RATINGS

( $T_A=25^{\circ}\text{C}$  Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	$V_{DSS}$	110	V	
Gate –Source Voltage	$V_{GSS}$	$\pm 20$	V	
Continuous Drain Current( $T_J=150^{\circ}\text{C}$ )	$I_D$	$T_A=25^{\circ}\text{C}$	90	A
		$T_A=70^{\circ}\text{C}$	90	
Pulsed Drain Current	$I_{DM}$	240	A	
Avalanche Current	$I_{AS}$	60	A	
Power Dissipation	$P_D$	$T_A=25^{\circ}\text{C}$	62.5	W
		$T_A=70^{\circ}\text{C}$	3.38	
Avalanche Energy with Single Pulse ( $T_J=25^{\circ}\text{C}$ , $L = 0.12\text{mH}$ , $I_{AS} = 75\text{A}$ , $V_{DD} = 80\text{V}$ .)	$E_{AS}$	335	mJ	
Operating Junction Temperature	$T_J$	-55/150	$^{\circ}\text{C}$	
Storage Temperature Range	$T_{STG}$	-55/150	$^{\circ}\text{C}$	
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	2	$^{\circ}\text{C}/\text{W}$	



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### ELECTRICAL CHARACTERISTICS

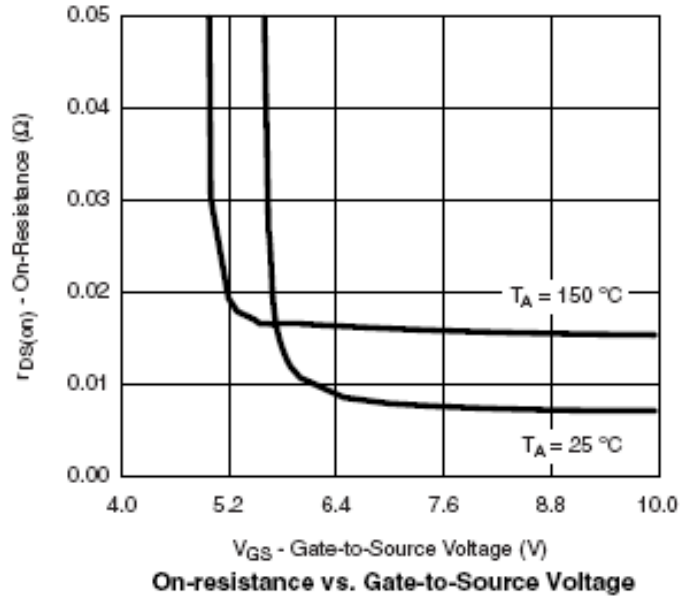
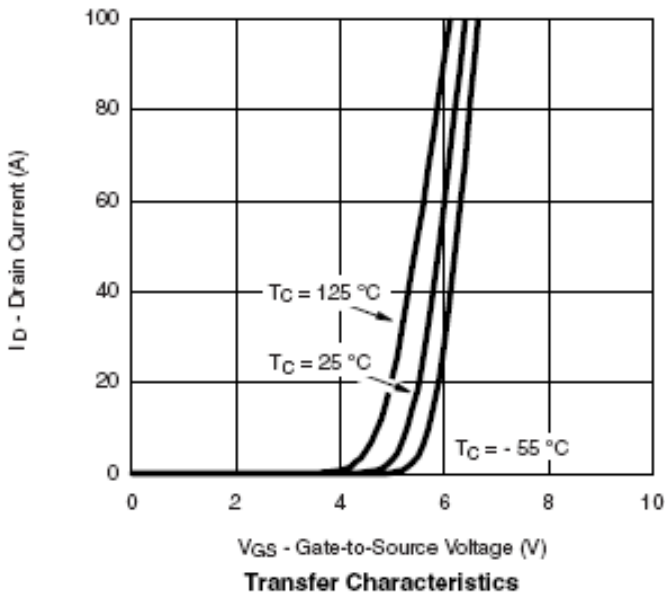
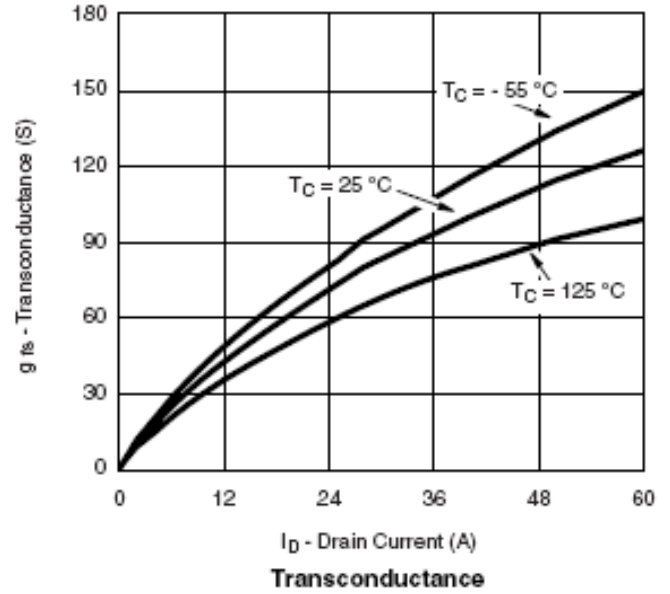
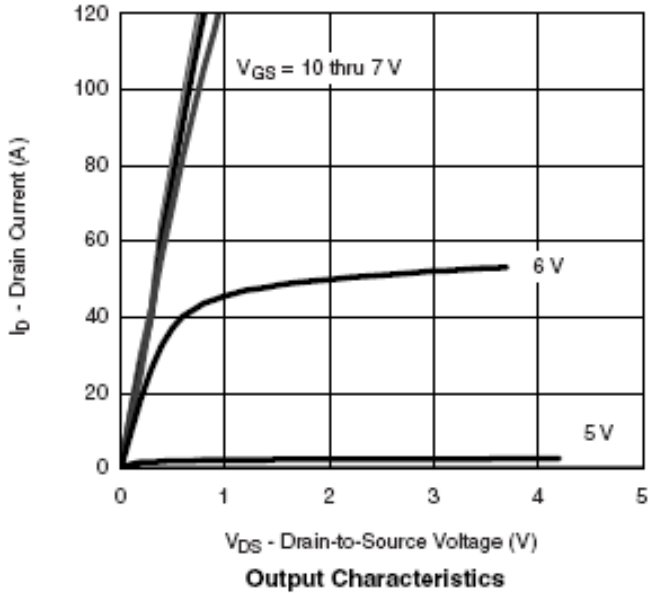
(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0		3.0	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μA
		V <sub>DS</sub> =100V, V <sub>GS</sub> =0V T <sub>J</sub> = 150 °C			250	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥10V, V <sub>GS</sub> =10V	70			A
Drain-Source On-Resistance	R <sub>DSS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> =80A		7.5	8.5	mΩ
		V <sub>GS</sub> = 6.0V, I <sub>D</sub> =30A		8.5	9.8	
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> =10A		8.2	10.0	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =20A		62		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =30A, V <sub>GS</sub> =0V			1.5	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V I <sub>D</sub> = 20A		100		nC
Gate-Source Charge	Q <sub>gs</sub>			35		
Gate-Drain Charge	Q <sub>gd</sub>			25		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V f=1MHz		6500		pF
Output Capacitance	C <sub>oss</sub>			650		
Reverse Transfer Capacitance	C <sub>rss</sub>			190		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, R <sub>L</sub> =0.6Ω I <sub>D</sub> =20A, V <sub>GEN</sub> =10V R <sub>G</sub> =1.0Ω		25		nS
	t <sub>r</sub>			20		
Turn-Off Time	t <sub>d(off)</sub>			30		
	t <sub>f</sub>			10		



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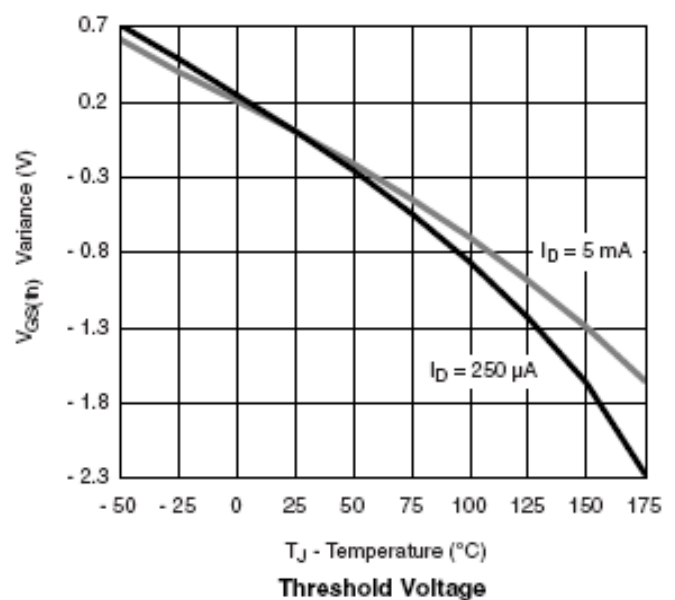
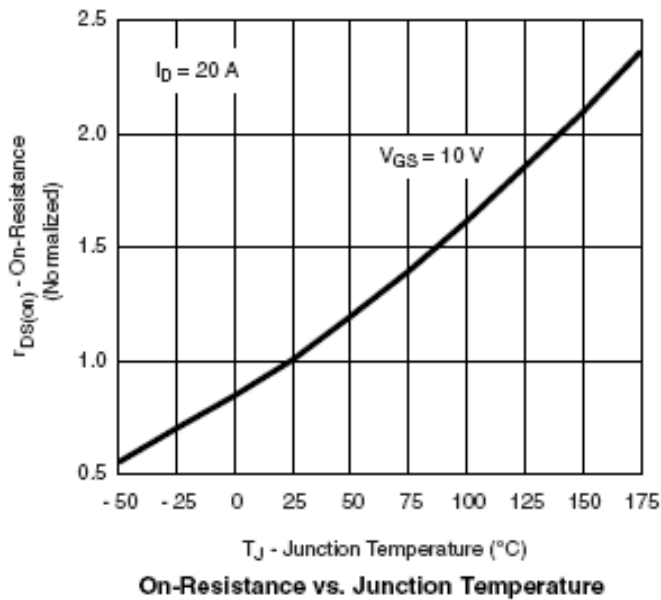
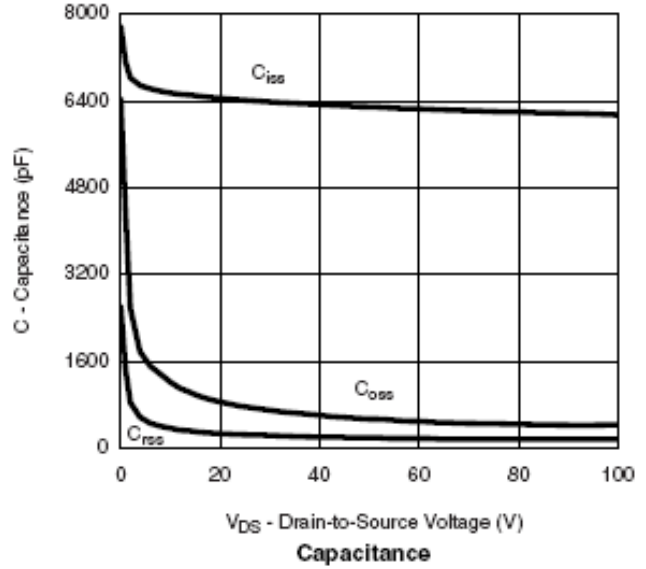
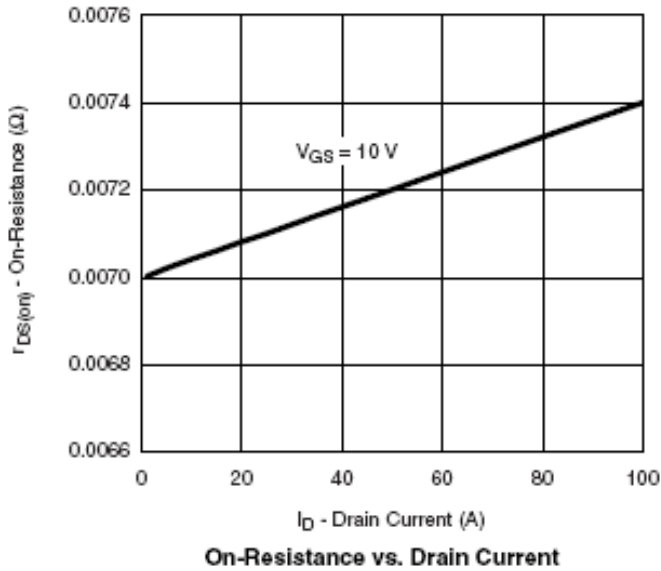
## TYPICAL CHARACTERISTICS





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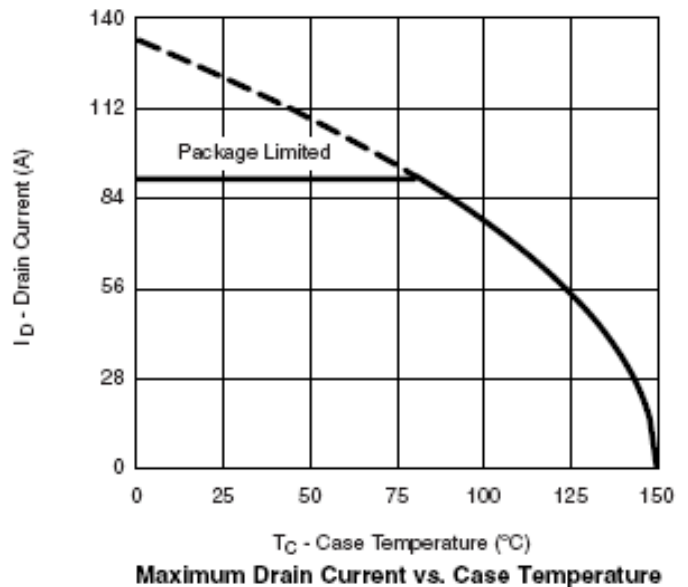
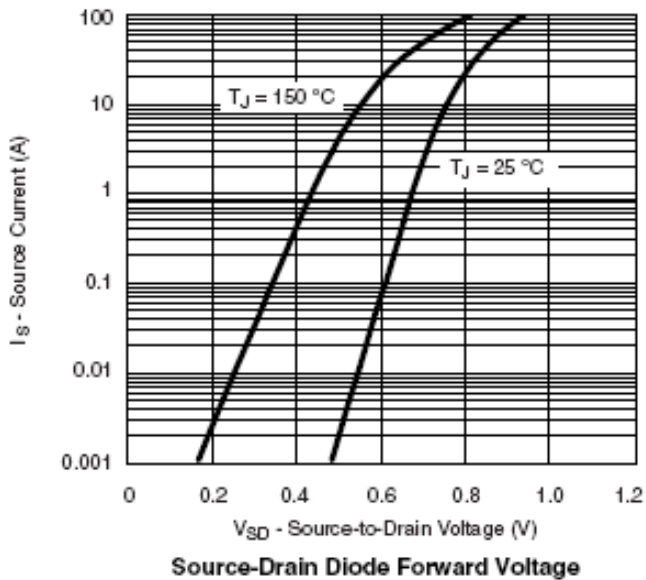
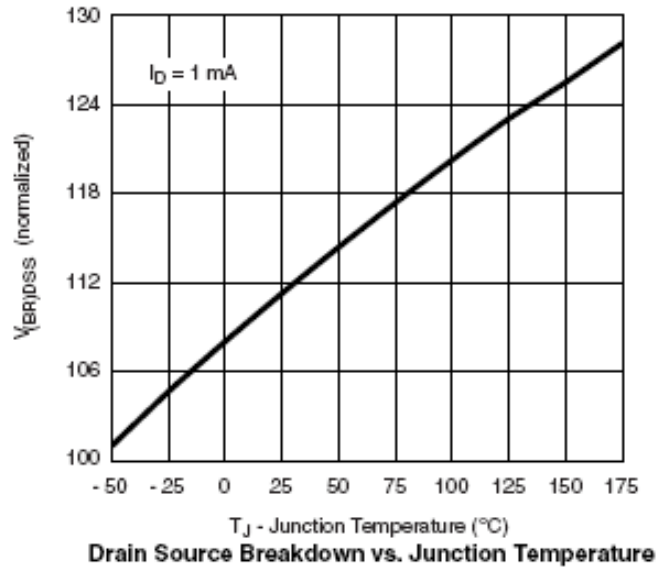
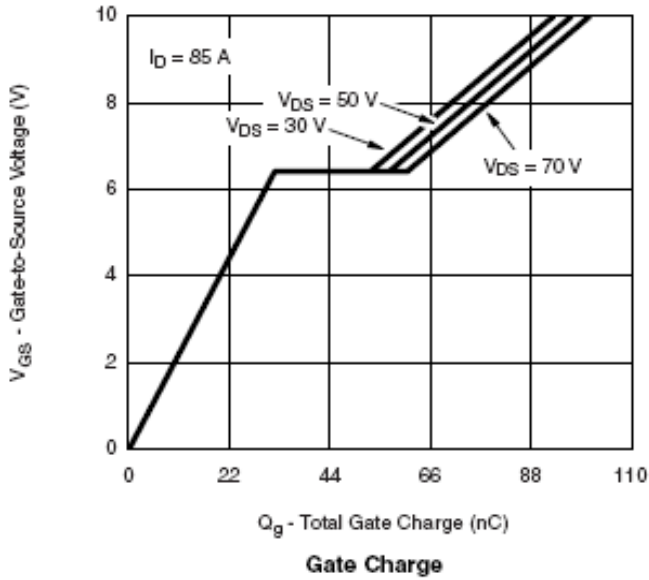
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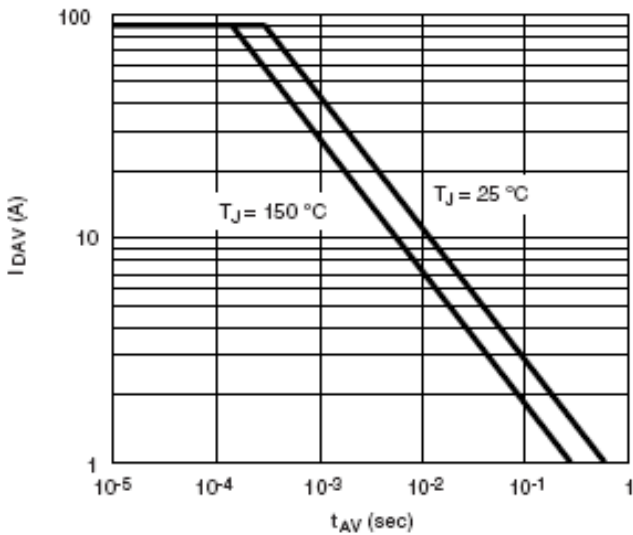
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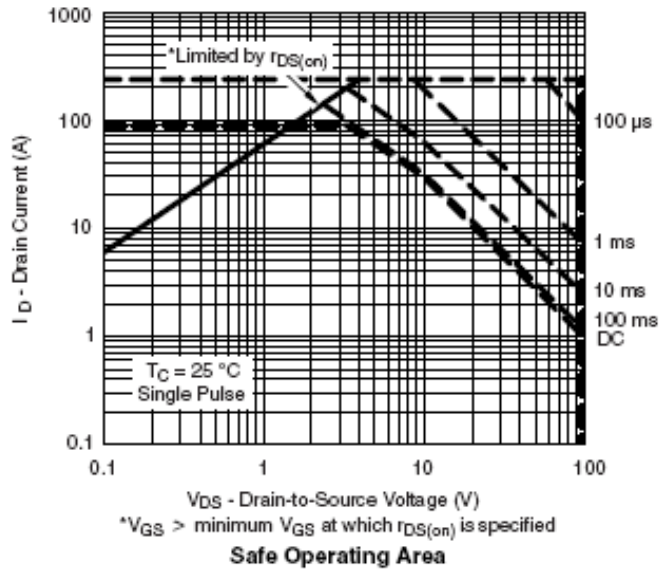


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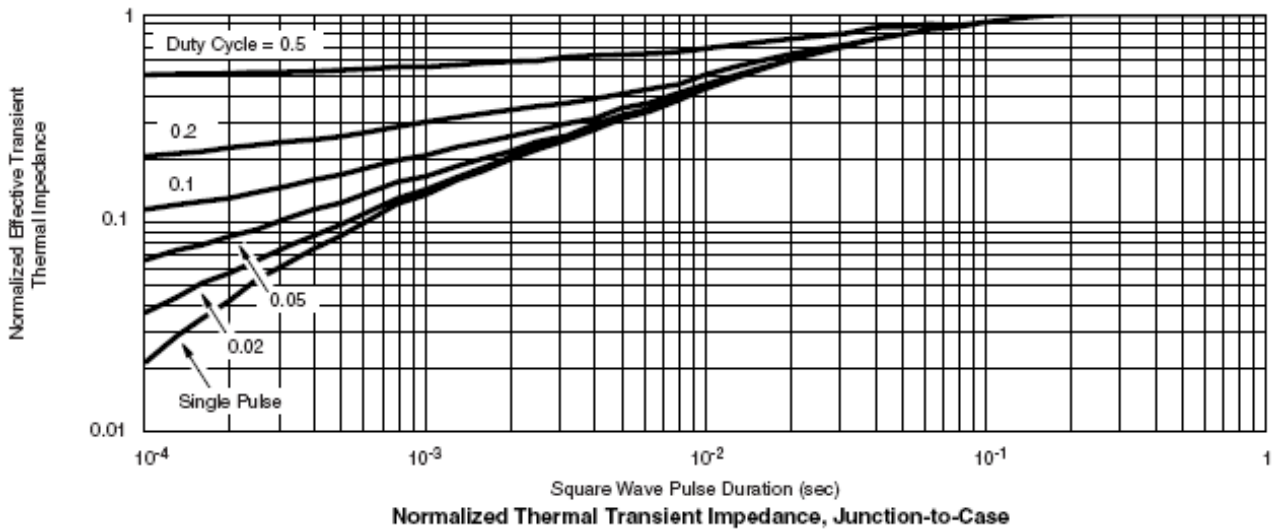
## TYPICAL CHARACTERISTICS



Single Pulse Avalanche Current Capability vs. Time



Safe Operating Area

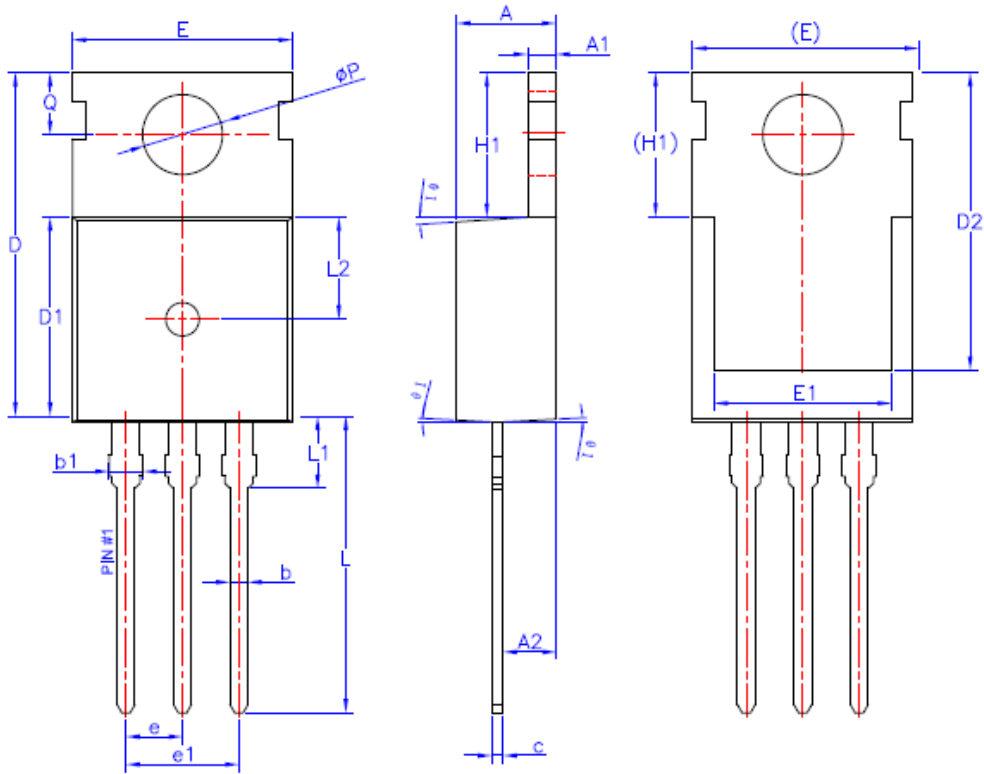


Normalized Thermal Transient Impedance, Junction-to-Case



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## TO-220-3L PACKAGE OUTLINE



SYMBOL	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	—	0.90
b1	1.42	—	1.57
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	—	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	—	—	3.50
L2	4.60REF		
φP	3.55	3.60	3.65
Q	2.73	—	2.87
θ1	1°	3°	5°





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