



# SPN5003 N-Channel Enhancement Mode MOSFET

### DESCRIPTION

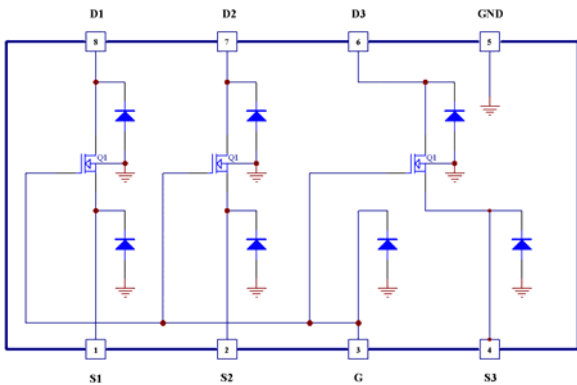
The SPN5003 is the N-Channel logic enhancement mode power field effect transistor which is produced with high voltage BiCMOS technology. This device is particularly suited for reducing the no load consumption in PC power, TV power and Adapter.

### APPLICATIONS

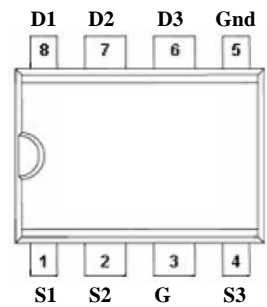
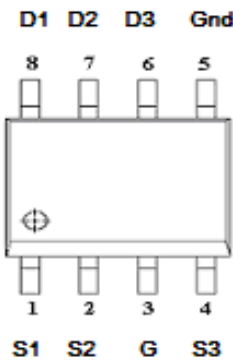
- Desk PC Power Supply
- AC adapter
- LCD TC Power Supply
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### FEATURES

- ◆ 500V/10mA,  $R_{DS(ON)}=250\Omega@V_{GS}=10V$
- ◆ Reduce power consumption at no load for EPA/Climate Saver Application
- ◆ SOP-8 and DIP-8 package design



### PIN CONFIGURATION



### PART MARKING



A: Lot Code  
B: Date Code



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### PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	S2	Source 2
3	G	Gate
4	S3	Source 3
5	Ground	Ground
6	D3	Drain 3
7	D2	Drain 2
8	D1	Drain 1

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN5003S8RGB	SOP-8	SPN5003
SPN5003D8TGB	DIP-8	SPN5003

※ SPN5003S8RGB : Tape Reel ; Pb – Free ; Halogen - Free

※ SPN5003D8TGB : Tube ; Pb – Free ; Halogen - Free

### ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	500	V	
Gate –Source Voltage	V <sub>GSS</sub>	+20	V	
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	TA=25°C	30	mA
		TA=70°C	20	
Pulsed Drain Current	I <sub>DM</sub>	200	mA	
Power Dissipation	P <sub>D</sub>	TA=25°C	1.3	W
		TA=70°C	1.0	
Operating Junction Temperature	T <sub>J</sub>	150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C	
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	80	°C/W	



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### ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=40\mu A$	500			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.65		1.8	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=+20V$			20	$\mu A$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=400V, V_{GS}=0V$			25	$\mu A$
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5V, V_{GS} = 10V$	25			mA
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10mA$			250	$\Omega$
		$V_{GS}=5.0V, I_D=10mA$			250	$\Omega$
Diode Forward Voltage	$V_{SD}$	$I_S=10mA, V_{GS} = 0V$		0.8	1.0	V
<b>Dynamic</b>						
Gate-Source Charge	$Q_{gs}$	$V_{DS}=50V, V_{GS}=10V$ $I_D= 25mA$		1		nC
Turn-On Time	$t_{d(on)}$	$V_{DD}=50V, R_L=2.7\Omega$ $V_{GEN}=5V, R_G=3\Omega$		20		nS
	$t_r$			16		
Turn-Off Time	$t_{d(off)}$			4		
	$t_f$			3.8		



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