



SPN55T20

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN55T20 is the N-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

FEATURES

- ◆ 200V/42A, $R_{DS(ON)}=32m\Omega@V_{GS}=10V$
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L/TO-252-2L/PPAK5x6-8L package design

APPLICATIONS

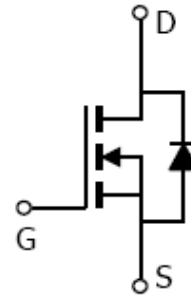
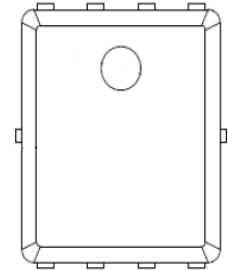
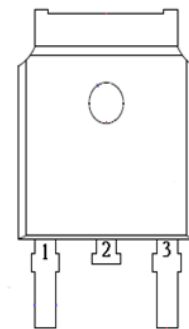
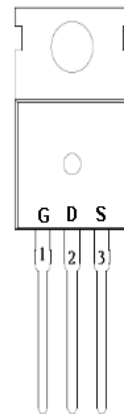
- DC/DC Converter
- Load Switch
- Synchronous Buck Converter
- SMPS Secondary Side Synchronous Rectifier
- Power Tool
- Motor Control

PIN CONFIGURATION(PPAK5x6-8L)

TO-220-3L

TO-252-2L

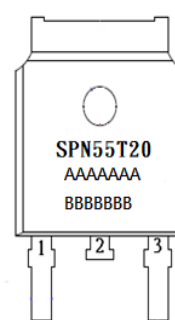
PPAK5x6-8L



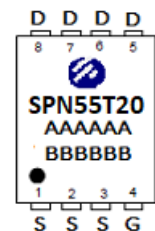
PART MARKING



A : Lot Code
B : Date Code



A : Lot Code
B : Date Code



A : Lot Code
B : Date Code
(YY / MM / DD)



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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

PPAK5x6 PIN DESCRIPTION

Pin	Symbol	Description
1	S	Source
2	S	Source
3	S	Source
4	G	Gate
5	D	Drain
6	D	Drain
7	D	Drain
8	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN55T20T220TGB	TO-220-3L	SPN55T20
SPN55T20T252RGB	TO-252-2L	SPN55T20
SPN55T20DN8RGB	PPAK5x6-8L	SPN55T20

- ※ SPN55T20T220TGB : Tube ; Pb – Free ; Halogen – Free
- ※ SPN55T20T252RGB : Tape Reel ; Pb – Free ; Halogen – Free
- ※ SPN55T20DN8RGB : 13” Tape Reel ; Pb – Free ; Halogen – Free



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ABSOLUTE MAXIMUM RATINGS

($T_A=25^{\circ}\text{C}$ Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	V_{DS}	200	V	
Gate-Source Voltage	V_{GS}	± 20	V	
Continuous Drain Current(Silicon Limited)	I_D	$T_C=25^{\circ}\text{C}$	42	A
		$T_C=100^{\circ}\text{C}$	26	
Pulsed Drain Current	I_{DM}	140	A	
Avalanche Energy, Single Pulse ($L=0.4\text{mH}, T_C=25^{\circ}\text{C}$)	E_{AS}	180	mJ	
Power Dissipation (TO-220)	P_D	104	W	
Power Dissipation (TO-252)		$T_C=25^{\circ}\text{C}$		93
Power Dissipation (PPAK5x6)		83		
Operating Junction Temperature	T_J	-55/150	$^{\circ}\text{C}$	
Storage Temperature Range	T_{STG}	-55/150	$^{\circ}\text{C}$	
Thermal Resistance-Junction to Case (TO-220)	$R_{\theta JC}$	0.85	$^{\circ}\text{C}/\text{W}$	
Thermal Resistance-Junction to Case (TO-252)	$R_{\theta JC}$	1.35	$^{\circ}\text{C}/\text{W}$	
Thermal Resistance-Junction to Case (PPAK5x6)	$R_{\theta JC}$	1.5	$^{\circ}\text{C}/\text{W}$	



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ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

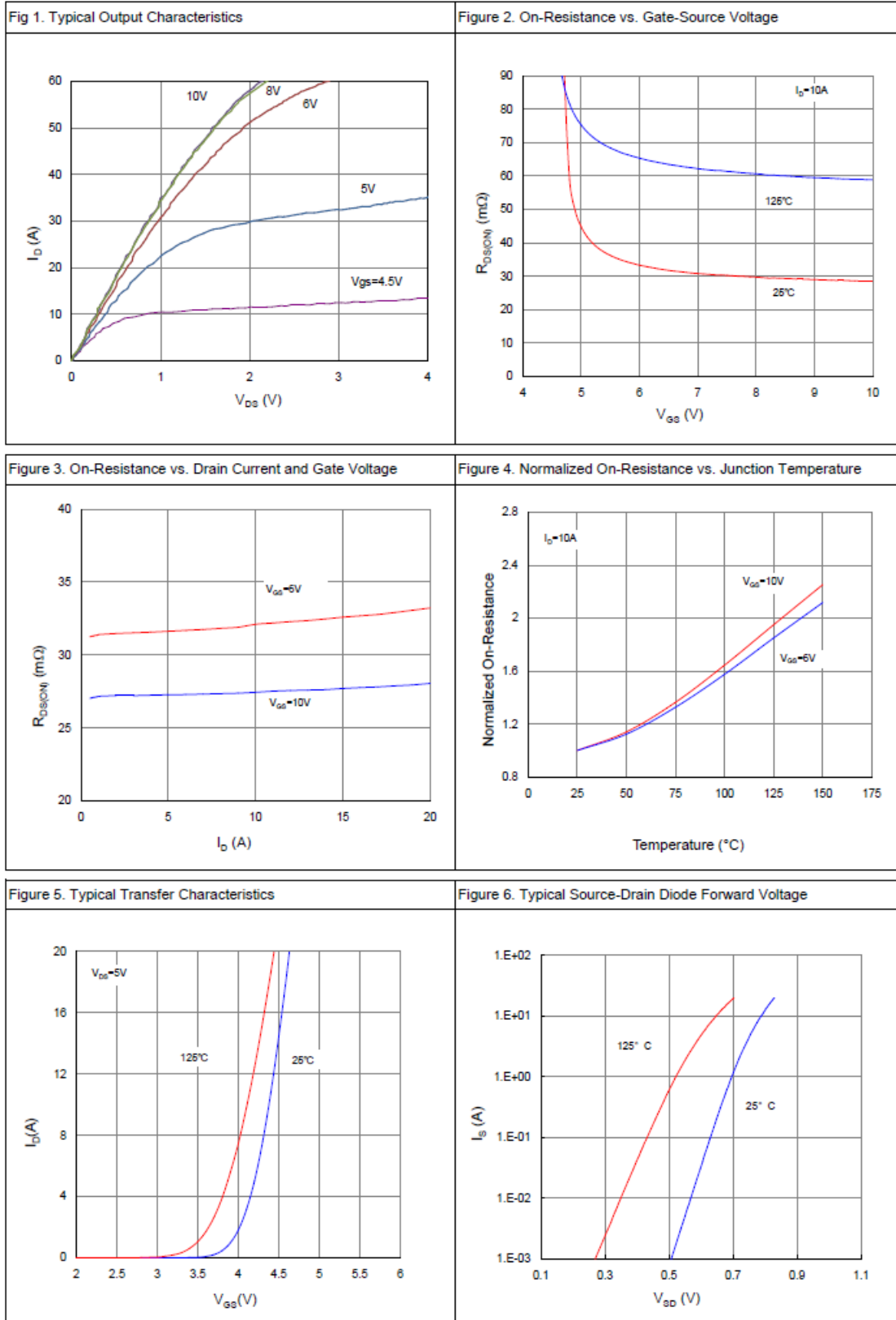
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=160V, V_{GS}=0V$ $T_J=25^\circ C$			1	uA
		$V_{DS}=160V, V_{GS}=0V$ $T_J=100^\circ C$			100	
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10A$		28	32	mΩ
Transconductance	g_{fs}	$V_{DS}=5V, I_D=10A$		31		S
Gate Resistance	R_G	$V_{GS}=0V, V_{DS}=\text{Open},$ $f=1\text{MHz}$		4.6		Ω
Diode Forward Voltage	V_{SD}	$I_S=10A, V_{GS}=0V$		0.9	1.2	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=100V, V_{GS}=10V$ $I_D=10A$		19		nC
Gate-Source Charge	Q_{gs}			7		
Gate-Drain Charge	Q_{gd}			2		
Input Capacitance	C_{iss}	$V_{DS}=100V, V_{GS}=0V$ $f=1\text{MHz}$		1598		pF
Output Capacitance	C_{oss}			124		
Reverse Transfer Capacitance	C_{rss}			7.5		
Turn-On Time	$t_{d(on)}$	$V_{DD}=100V,$ $I_D=10A, V_{GS}=10V$ $R_G=10\Omega$		12		nS
	t_r			17		
Turn-Off Time	$t_{d(off)}$			23		
	t_f			10		



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TYPICAL CHARACTERISTICS

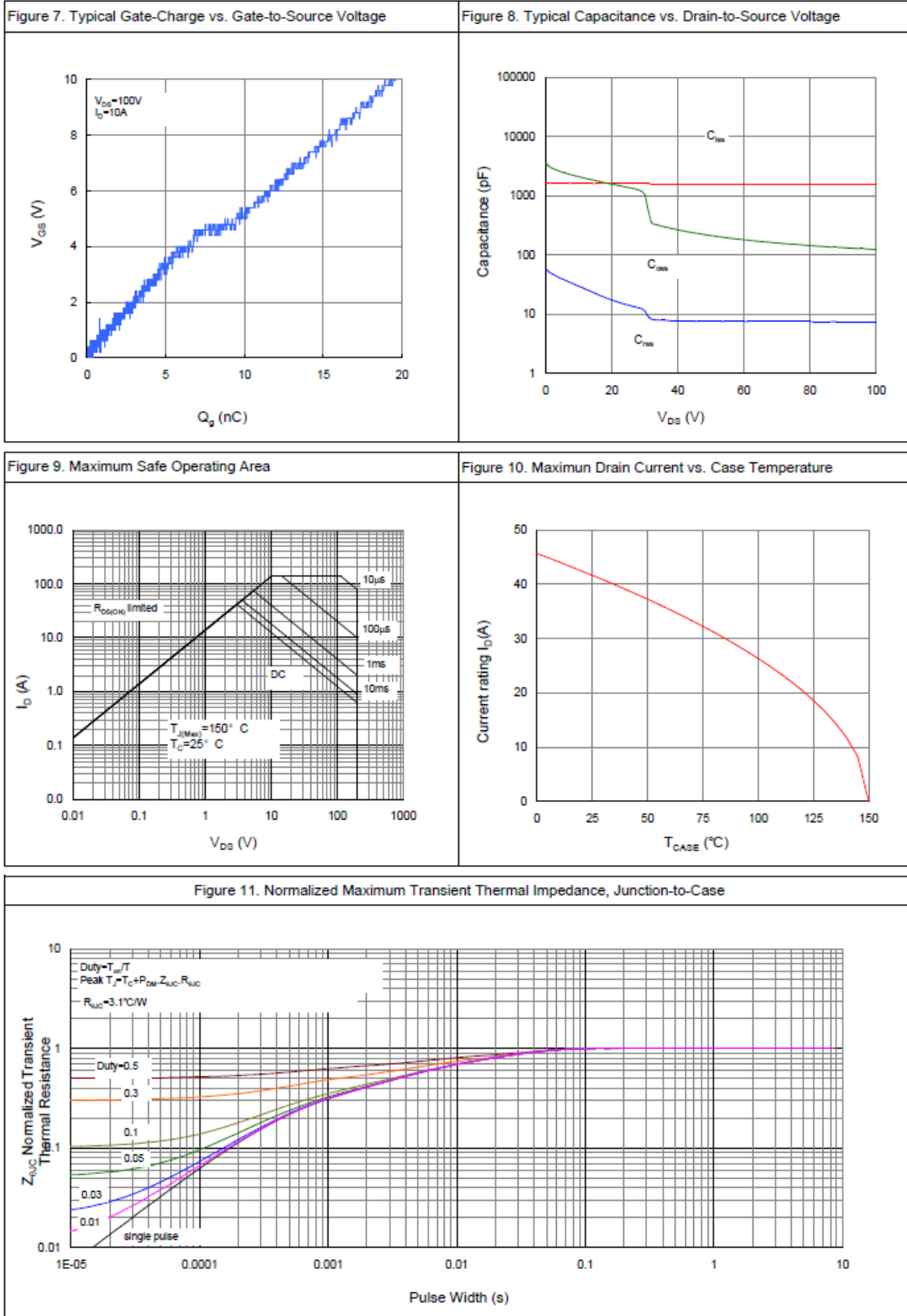




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TYPICAL CHARACTERISTICS





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