

#### **DESCRIPTION**

The SPN6232 is the N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

# APPLICATIONS

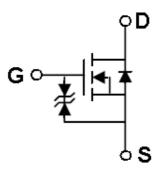
- Drivers : Relays/Solenoids/Lamps/Hammers
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers

#### **FEATURES**

- N-Channel 30V/0.95A, RDS(ON)= $400m\Omega$ @VGS=4.5V 30V/0.75A, RDS(ON)= $500m\Omega$ @VGS=2.5V 30V/0.65A, RDS(ON)= $650m\Omega$ @VGS=1.8V
- Super high density cell design for extremely low RDS (ON)
- Exceptional on-resistance and maximum DC current capability
- ESD Protected
- DFN1.0x0.6-3L(SOT-883) package design

# PIN CONFIGURATION(DFN1.0x0.6-3L)





**PART MARKING** 

2YW

PIN DESCRIPTION						
Pin	Symbol	Description				
1	G	Gate				
2	S	Source				
3	D	Drain				

# **ORDERING INFORMATION**

Part Number	Package	Part Marking		
SPN6232DN3RGB	DFN1.0x0.6-3L	2		

<sup>※</sup> SPN6232DN3RGB: Tape Reel; Pb − Free, Halogen − Free

# ABSOULTE MAXIMUM RATINGS

(Ta=25°C Unless otherwise noted)

Parameter		Symbol	Typical	Unit	
Drain-Source Voltage		Vdss	30	V	
Gate –Source Voltage		VGSS	±12	V	
Continue David Consultation 150°C	Ta=25°C	τ_	1.2	A	
Continuous Drain Current(T <sub>J</sub> =150°C)	Ta=125°C	ID	1.0		
Pulsed Drain Current (*)		IDM	2.5	A	
Thermal Resistance-Junction to Ambient		R <sub>θ</sub> JA	375	°C/W	
Power Dissipation	Ta=25°C	PD	1.35	W	
Operating Junction Temperature		Тл	-55/150	$^{\circ}\!\mathbb{C}$	
Storage Temperature Range		Tstg	-55/150	$^{\circ}\!\mathbb{C}$	

<sup>(\*)</sup> Pulse width limited by safe operating area

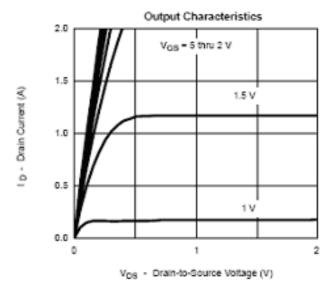
# **ELECTRICAL CHARACTERISTICS**

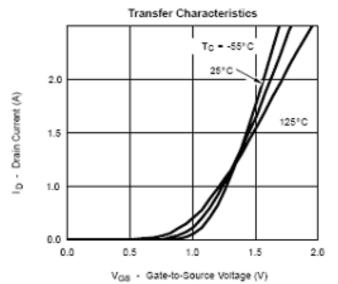
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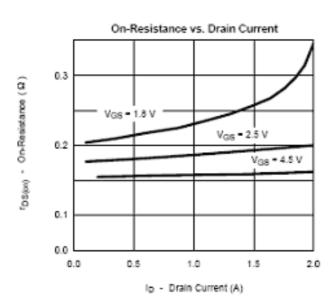
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V(BR)DSS	(BR)DSS VGS=0V,ID= 250uA 30				17	
Gate Threshold Voltage	V <sub>GS</sub> (th)	Vds=Vgs,Id=250uA	0.45		1.0	V	
Gate Leakage Current	Igss	VDS=0V,VGS=±12V			±10	uA	
Zero Gate Voltage Drain Current	T	VDS=24V,VGS=0V,TJ=25°C			1		
	IDSS	VDS=24V,VGS=0V,TJ=55°C			100	uA	
On-State Drain Current	ID(on)	$V_{DS} \ge 4.5V, V_{GS} = 5V$	0.7			A	
Drain-Source On-Resistance		Vgs=4.5V,Id=0.95A		0.26	0.40		
	RDS(on)	V <sub>GS</sub> =2.5V,I <sub>D</sub> =0.75A		0.38	0.50	Ω	
		VGS=1.8V,ID=0.65A		0.52	0.65		
Forward Transconductance	gfs	Vds=10V,Id=0.4A		1.0		S	
Diode Forward Voltage	Vsd	Is=0.15A,VGS=0V		0.8	1.2	V	
Dynamic							
Total Gate Charge	Qg			1.2	1.5	nC	
Gate-Source Charge	Qgs	VDS=10V,VGS=4.5V, ID=0.6A		0.2			
Gate-Drain Charge	Qgd	ID=0.0A		0.3			
Input Capacitance	Ciss			7.2			
Output Capacitance	Coss	VDS=10V,f=1MHz,VGS=0V		13.5		pF	
Reverse Transfer Capacitance	Crss			1.6			
Turn-On Time	td(on)	Vpp_10VDr=100		5	10	nS	
	tr	$-V_{DD}=10V,RL=10\Omega$ , $I_{D}=0.5A,V_{GEN}=4.5V$		8	15		
Turn-Off Time	td(off)	,RG=6Ω		10	18		
	tf			1.2	2.8		

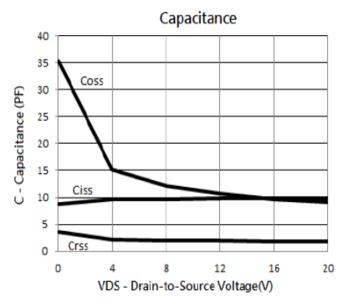
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#### TYPICAL CHARACTERISTICS







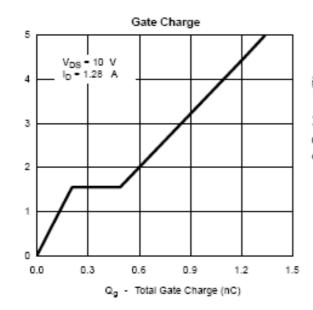


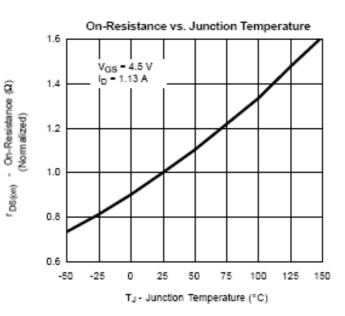
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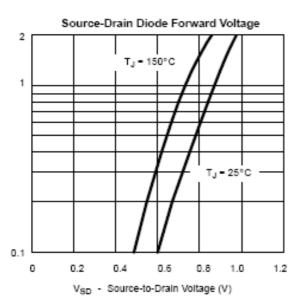
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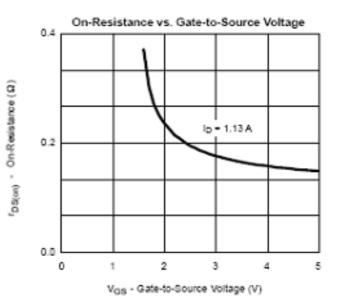
V<sub>GS</sub> - Gate-to-Source Voltage (V)

Is - Source Current (A)



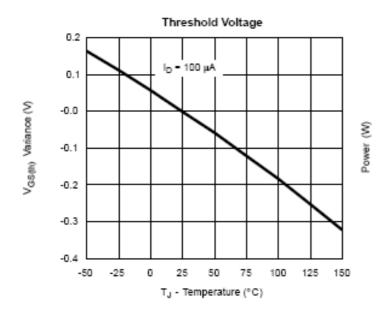


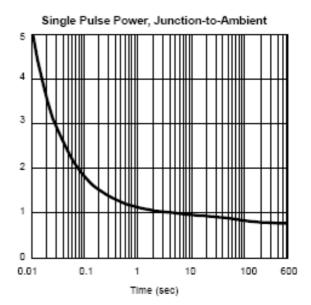




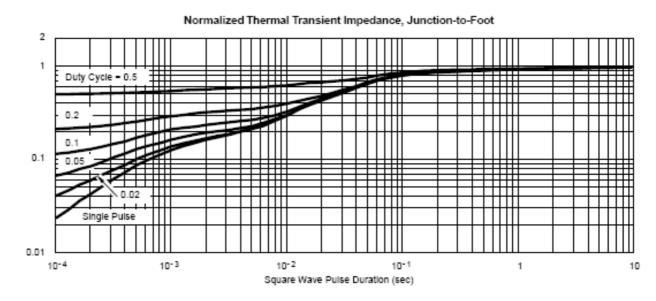
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# TYPICAL CHARACTERISTICS





# Normalized Effective Transient Thermal Impedance



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